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2
3 ;=====
4 := PRIMER OPERATING SYSTEM v 2.7 =
5 := Copyright 1991-1996 EMAC Inc. =
6 ;=====
7
8 ;
9 ;ver 2.3 (6/3/93) Gives access to DDATA, and FUN1 in the MOS services and
10 ; adds EPROM burner code. Pressing "Func.", "2" does a CALL 1000
11 ; without changing the PC register.
12 ;
13 ;ver 2.4 (3/1/94) changes a/d routine to successive approximation algorithm,
14 ; more robust UART test, UART loopback test.
15 ;
16 ;ver 2.5 Ports over better EPROM burner code from EMOS 1.7 and
17 ; adds hex download function key 3 to the MOS.
18 ;
19 ;ver 2.6 Changed serial protocol to 1 stop bit instead of 2.
20 ;
21 ;
22 ;ver 2.7 (2/13/96) Added menu driven EPROM programmer.
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; adds EPROM burner code. Pressing "Func.", "2" does a CALL 1000
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;
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begram: equ 0ff00h ; beginning of user ram
maxram: equ 0ffffh ; end of ram

sysbyts: equ 25 + 1
monstk: equ maxram - sysbyts ; start of monitor stack-#
; of bytes of sys vars

userstk: equ maxram - 0fh - sysbyts
maxrom: equ 40h ; maximum high byte of rom address (16k)
porta: equ 11h
leds: equ 11h
portb: equ 12h
dip: equ 12h
portc: equ 13h
iocreg: equ 10h

; ports to send the frequency
fprtlo: equ 14h
fprthi: equ 15h

; serial port
serdta: equ 80h
sercom: equ 81h

cr equ 13 ; carriage return

aseg org 0
; start
; 'v2.7'
;
; rst1
start1
; '3/96'
;
; rst2
org 10h
start2
;
; rst3
org 18h
start3
;
; rst4
org 20h
start4
;
; rst5
org 28h
start5
;
; 5.5
; the code couldn't be put here because between
; 2ch and 30h there are only 4 bytes, so jump to it
org 2ch
fivglf
;
; rst6
org 30h
start6
;
; 6.5 interrupt is used for single stepping
org 34h
sixhlf
;
; when rst7, (software bp) jump to monitor
org 38h
bpretry
;
; 7.5
; save hl
; hl = address of interrupt service routine
; put address on stack, and restore hl
; jump to address on stack
org 3ch
push h
lhd vec7hlf
xthl
ret

fivglf: push h ; save hl
lhd vec5hlf ; hl = address of interrupt service routine
xthl ; put address on stack, and restore hl
ret ; jump to address on stack

sixhlf: push h ; save hl
lhd vec6hlf ; hl = address of interrupt service routine
xthl ; put address on stack, and restore hl
ret ; jump to address on stack

org 4eh ; Start of primer operating system

start1:
start2:
start3:
start4:
start5:
start6:
start:

109 004E 31 FFE5 lxi sp,monstk ; point at monitor stack
110 0051 3E 0F mvi a,0fh ; disable all interrupts
111 0053 30 sim ; set up the ppi
112 ; set up the ppi
113 0054 3E CD mvi a,0cdh
114 0056 D3 10 out iocreg
115 0058 AF xra a
116 0059 D3 12 out portb
117 005B 3D 11 dcr a ; store FF to PORTA
118 005C D3 11 out porta
119 ; set up the uart (it may not exist)
120 xra a
121 005E AF out sercom
122 005F D3 81 out sercom
123 0061 D3 81 out sercom
124 0063 D3 81 out sercom
125 0065 3E 40 mvi a,40h ; reset internal
126 0067 D3 81 out sercom
127 0069 3E 4E mvi a,01001110b ; 1 stop, no parity, 8 data, 1/16
128 006B D3 81 out sercom
129 006D 3E 27 mvi a,00100111b ; set RTS, DTR and enable TX and RX
130 006F D3 81 out sercom
131 ; Initialize the display section of the 8279
132 0071 3E 00 mvi a,0
133 0073 D3 41 out dspcmd
134 0075 3E 3F mvi a,3fh
135 0077 D3 41 out dspcmd
136 0079 3E C1 mvi a,0clh
137 007B D3 41 out dspcmd
138 ; delay for a moment
139 lxi h,04000h
140 007D 21 4000 call dlay
141 0080 CD 0181 mvi a,80h
142 0083 3E 80 out dspcmd
143 0085 D3 41 out dspcmd
144 0087 C3 2F01 jmp moscode ; jump to start of mos or target app

; DIGIT2 puts number from rdkey into the low nibble of l after shifting
; l l nibble left.
digit2: mov b,a ; b= byte from rdkey

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151 008B 7D          mov     a,l      ; shift 1 nibble left
152 008C 0F          rrc     ;
153 008D 0F          rrc     ;
154 008E 0F          rrc     ;
155 008F 0F          rrc     ; shift number into hi nibble
156 0090 C3          jmp     twonib
157
158                ;Same as digit2 but shift hl left 1 nibble
159 0093 47          digit4: mov    b,a      ; b= byte from rdkey
160 0094 29          dad     h
161 0095 29          dad     h
162 0096 29          dad     h
163 0097 29          dad     h
164 0098 7D          mov     a,l      ; rotate left 1 nibble ( 4 bits )
165
166 0099 E6          twonib: ani   0f0h    ; clear lo nibble
167 009B B0          ora     b        ; put new nibble into low byte
168 009C 6F          mov     l,a      ; save in l
169 009D C9          ret
170
171                ;
172                ;
173                ; DISPLAY DRIVERS
174                ;
175
176
177 009E 17          C1 45 8D 00 00  FUNMSG:  DEFB     17h,0clh,45h,08dH,0,0 ; "Func.____"
178
179                ; bit patterns for register pairs
180 00A4 7F          1F CF 9B  regmsg:  defb    07fh,01fh, 0cfh,09bh ; af,bc
181 00A8 ED          9F 6F 8B      defb    0edh,09fh, 06fh,08bh ; de,hl
182 00AC DE          3F 3F 9B      defb    0deh,03fh, 03fh,09bh ; sp,pc
183 00B0 CF          3F DE 9B      defb    0cfh,03fh, 0deh,09bh ; bp,sc
184
185 00B4 0D          ED          defb    0dh,0edh ; reg 8 "rd" for ram diagnostics
186 00B6 CF          0D          defb    0cfh,0dh ; reg 9 "br" for bad ram
187 00B8 9F          ED          defb    09fh,0edh ; reg 10 "E.d." for EPROM diagnostics
188 00BA CF          9F          defb    0cfh,09fh ; reg 11 "b.E." for bad EPROM
189 00BC CF          0B          defb    0cfh,0deh ; reg 12 "b.S." for bad serial
190 00BE 7B          EB          defb    07bh,0ebh ; reg 13 "N.U." for no UART
191 00C0 8B          8B          defb    8bh,8bh ; reg 14 "L.L." for local loopback
192
193
194                ; prints the message "Func." in the left 4 displays
195 00C2 06          funprnt: mvi    b,rgtdsp+5 ; point to left display
196 00C4 21          85          lxi    h,funmsg
197 00C7 0E          009E        mvi    c,6 ; number of bytes in msg
198 00C9 C3          00DA        jmp    funl
199
200                ; print the register names in the right two displays
201                ; Upon entry, a=register number 0-7
202                ; af = 0, bc=1, de=2, hl=3, sp=4,pc=5,brk = 6, sc = 7
203                ; af,bc,hl are used
204
205 00CC 06          81          regprnt: mvi    b,rgtdsp+1 ; point to the second digit from right
206 00CE 21          00A4        regprnt: lxi    h,regmsg
207 00D1 07          rlc     ;
208 00D2 85          add     ; a=a*2
209 00D3 6F          mov     l,a
210 00D4 3E          00          mvi    a,0
211 00D6 8C          adc     h
212 00D7 67          mov     h,a ; hl=hl+2*a
213 00D8 0E          02          mvi    c,2 ; print 2 chars
214
215 00DA 78          funl:    mov     a,b ; select display
216 00DB D3          41          out    dspcmd ; get bit map from (hl)
217 00DD 7E          inx     h
218 00DE 23          out     dspout ; output bit pattern to the display
219 00DF D3          40          dcr    b
220 00E1 05          dcr    c
221 00E2 0D          jnz    funl ; loop until c=0
222 00E3 C2          00DA
223 00E6 C9          ret
224
225 00E7 F3          60 B5 F4 66 D6  dmap:  defb    0f3h,60h,0b5h,0f4h,66h,0d6h,0d7h,70h,0f7h
226 00F0 F6          77 C7 93 E5 97  defb    0f6h,77h,0c7h,93h,0e5h,97h,17h ; zero thru F
227
228                ; This outputs the digit in A (0-f) to the display number in B (80-85 hex)
229                ; only hl is preserved
230
231 00F7 E5          digout:  push   h
232 00F8 21          00E7        lxi    h,dmap ; point to bit map table
233 00FB 85          add     l ; add A to HL
234 00FC 6F          mov     l,a
235 00FD 3E          00          mvi    a,0
236 00FF 8C          adc     h
237 0100 67          mov     h,a ; hl=hl+a
238
239 0101 78          mov     a,b ; b= 80h-85h
240 0102 D3          41          out    dspcmd ; select display
241 0104 7E          inx     h ; get bit map from (hl)
242 0105 D3          40          out     dspout ; output bit pattern to the display
243
244 0107 E1          pop     h
245 0108 C9          ret
246
247 0060=          rdrtdsp  equ     60h ; 8279 internal addr of right display for reading
248 0080=          rgtdsp  equ     80h ; 8279 internal address of right display
249 0041=          dspcmd  equ     41h ; i/o addr of display command
250 0040=          dspout  equ     40h ; i/o addr of display output
251 0040=          keyin  equ     40h ; i/o addr of scanned key
252
253                ; DDATA displays value in A on the right two displays
254                ; DISBYT displays a byte at the display pair pointed to by B
255 0109 06          80          ddata:  mvi    b,rgtdsp ; select rightmost digit
256                ; DISBYT displays a byte at the display pair pointed to by B
257 010B 4F          disbyt: mov    c,a ; save A in c
258 010C E6          ani    0fh ; mask off left nibble
259 010E 0F          00F7        call   digout ; show A on rightmost display
260 0111 7D          mov    a,c ; restore original A
261 0112 0F          rrc    ;
262 0113 0F          rrc    ;
263 0114 0F          rrc    ;
264 0115 0F          rrc    ; these 4 move left nibble to right
265 0116 E6          ani    0fh ; mask off left nibble
266 0118 04          inx    b ; select digit to the left
267 0119 CD          00F7        call   digout
268 011C C9          ret
269
270                ; DADDR displays the address in hl on the left 4 displays
271                ; de is not changed
272 011D 06          82          daddr:  mvi    b,rgtdsp+2
273 011F 7D          mov    a,l
274 0120 CD          010B        call   disbyt ; display l
275 0123 04          inr    b ; move to next digit pair
276 0124 7C          010B        mov    a,h
277 0125 CD          call   disbyt ; display h
278 0128 C9          ret
279
280                ; RDKEY polls the 5.5 interrupt and if high it will get the keypad value and
281                ; return it in A
282                ; keys 0-f will return 00-0fh
283                ; 10h-13h are not used
284                ; step,func,dec,ent/inc = 14h-17h respectively
285                ; HL,DE NOT AFFECTED
286                ; no regs preserved
287
288 0014=          STEP:   EQU    14H
289 0015=          FUNC:  EQU    15H
290 0016=          DECP: EQU    16H
291 0017=          ENTER: EQU    17H
292
293 0129 DB          41          plkpad: in     dspcmd ; see if key waiting
294 012B E6          07          ani    7
295 012D C2          013D        jnz    rdkey1 ; calculate key val if key waiting
296 0130 3E          FF          mvi    a,0fh ; indicate no key ready
297 0132 C9          ret ; ret if key not waiting
298
299 0133 DB          41          rdkey:  IN     DSPCMD ; SEE IF A KEY IS WAITING (KEY BUFFER WILL BE > 0)

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300 0135 E6 07 ANI 07H
301 0137 CA 0133 jz rdkey ; loop until key pressed
302 013A CD 016C call beep
303
304 013D 3E 40 rdkey1: mvi a,40h
305 013F D3 41 out dspcmd
306 0141 DB 40 in keyin ; get the scanned key.
307 ; bit pattern is CNTRL,SHFT,3 bits SCAN,3 bits RETURN
308
309 0143 E6 3F ani 3fh ; bit 7 not used
310 0145 47 mov b,a ; preserve A
311 0146 E6 38 ani 00111000b ; masking off all but scan and..
312 0148 0F rrc ; dividing by 2 makes scan=scan*4
313 0149 4F mov c,a ; c=scan*4
314 014A 78 mov a,b ; A=original scanned key
315 014B E6 07 ani 00000111b ; mask all but the value for RETURN
316 014D 81 add c ; add to scan*4
317 014E C9 ret
318
319 ; Sound Port drivers
320 014F 06 C0 buzzon: mvi b,0c0h
321 0151 C3 0156 jmp sod
322 0154 06 40 buzzoff: mvi b,40h
323
324 0156 20 sod: rim
325 0157 E6 1F ani 1fh
326 0159 B0 ora b
327 015A 30 sim
328 015B C9 ret
329
330
331 ; Send the frequency in HL to sound chip. HL is limited to <=3ffff
332 015C 7D sdiv: mov a,l
333 015D D3 14 out fprtl0
334 015F 7C mov a,h
335 0160 3E 3F mvi a,3fh
336 0162 A4 ana h
337 0163 F6 40 ori 40h
338 0165 D3 15 out fpwrthi
339 0167 3E CD mvi a,0cdh
340 0169 D3 10 out iocreg
341 016B C9 ret
342
343 016C E5 beep: push h ; do a beep
344 016D 21 lxi h,0200h
345 0170 CD 015C call sdiv ; set the frequency
346 0173 CD 014F call buzzon
347 0176 21 3000 lxi h,03000h
348 0179 CD 0181 call dlay
349 017C CD 0154 call buzzoff
350 017F E1 pop h
351 0180 C9 ret
352
353 0181 2B dlay: dcx h
354 0182 7C mov a,h
355 0183 B5 l
356 0184 C2 jnz dlay
357 0187 C9 ret
358
359
360
361
362
363 ; monitor services: jump to the service number held in C
364 1000 mservices:
365 1000 F5 push psw
366 1001 E5 push h
367 1002 21 lxi h,servtbl
368 1005 79 mov a,c
369 1006 FE 25 cpi mxsrvm ; if C > mxsrvm then CY=0
370 1008 D2 1017 jnc serverr ; if cy = 0 then there is a service error
371 100B 87 add a ; service # *2 = index to service table
372 100C 85 add l
373 100D 6F mov l,a
374 100E 3E 00 mvi a,0
375 1010 8C adc h
376 1011 67 mov h,a ; hl = hl + c*2
377 1012 7E mov a,m
378 1013 23 inx h
379 1014 66 mov h,m
380 1015 6F mov l,a ; hl = service address
381 1016 E9 pchl ; jmp to hl
382
383 1017 E1 serverr: pop h
384 1018 F1 pop psw ; just return if bad service #
385 1019 C9 ret
386
387
388 1020 11 org 1020h
389 1023 2E 0304 lxi d,0304h ; This is a ROM resident example program
390 1025 63 06 mvi l,06h ; which the student may view and run
391 1026 24 mov h,e
392 1027 3E 01 inr h
393 1029 47 mvi a,l
394 102A 07 mov b,a
395 102B 4F rlc
396 102C 97 mov c,a
397 102D FF sub a
398 rst 7
399 102E 1078 10A2 10B1 servtbl: dw serv0,serv1,serv2,serv3,serv4,serv5
400 103A 1107 111F 114C dw serv6,serv7,serv8,serv9,servA,servB
401 1046 11CF 11D6 11FC dw servC,servD,servE,servF,serv10,serv11
402 1052 122C 1237 1245 dw serv12,serv13,serv14,serv15,serv16,serv17
403 105E 126F 13AA 148C dw serv18,serv1A,serv1B,serv1C,serv1D,serv1E,serv1F
404 106E 16E0 172E 1767 dw serv20,serv21,serv22,serv23,serv24
405 0025= mxsrvm: equ ($ - servtbl) / 2 ; this is the max service number
406
407 ; Demo program: This outputs an increasing frequency while flashing LEDs at
408 ; an increasing rate.
409 1078 C5 serv0: push b
410 1079 D5 push d
411 107A CD 014F call buzzon ; turn on the speaker
412 107D 01 3FFF lxi b,3ffff ; start with long delay and low freq.
413 1080 16 7F mvi d,07fh ; bit pattern to light 1 LED
414
415 1082 60 serv0a: mov h,b
416 1083 2E 00 mvi l,0
417 1085 CD 015C call sdiv ; set the frequency to hl
418 1088 CD 0181 call dlay ; delay according to hl
419 108B 7A mov a,d ; get bit pattern
420 108C 0F rrc ; rotate the bit pattern
421 108D 57 mov d,a ; save in d again
422 108E D3 11 out leds ; display the bit pattern
423 1090 05 dcr b ; increase freq. and decrease delay
424 1091 C2 1082 jnz serv0a ; loop until bc=0
425 1094 CD 0154 call buzzoff ; shut off the speaker
426 1097 3E FF mvi a,0fffh ; bit pattern for all LEDs off
427 1099 D3 11 out leds ; turn off the display
428 109B D1 pop d
429 109C C1 pop b
430 109D E1 pop h
431 109E F1 pop psw
432 109F C9 ret
433
434 ; This waits for a key to be typed at the terminal and returns it in L
435 10A0 F5 getkey: push psw
436 10A1 E5 push h
437
438 10A2 E1 serv1: pop h ; restore hl
439 10A3 DB 81 srvla: in sercom ; get serial port status
440 10A5 E6 02 ani 2 ; isolate receive ready bit
441 10A7 CA 10A3 jz srvla ; loop until bit set
442 10AA DB 80 in serdta ; get the character
443 10AC 6F mov l,a ; put char in l
444 10AD F1 pop psw ; restore psw
445 10AE C9 ret
446
447 ; This returns L holding a FF if a terminal key was pressed or 0 if not
448 10AF F5 polkey: push psw

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449 10B0 E5          push    h
450
451 10B1 E1          serv2:  pop    h          ; restore hl
452 10B2 2F          mwi    l,0
453 10B4 DB          in     sercom
454 10B6 E6          ani    2          ; isolate receive ready bit
455 10B8 CA          jz    serv2a     ; if 0, L is correct
456 10BB 2D          dcr    l          ; decrement to 0ffh
457 10BC F1          serv2a: pop    psw
458 10BD C9          ret
459
460
461 10BE F5          ; Same as service 3, but a direct call
462 10BF E5          conout: push   psw
463          push   h
464          ; This sends the character in E to the terminal display
465 10C0 DB          81          serv3:  in     sercom
466 10C2 E6          01          ani    1          ; isolate transmit ready bit
467 10C4 CA          10C0       jz    serv3     ; if 0, loop again
468 10C7 7B          mov    a,e       ; put char in a
469 10C8 D3          80          out    serdta    ; output to terminal
470 10CA E1          pop    h
471 10CC C9          pop    psw
472          ret
473
474          ; This reads the characters starting at the address in DE and sends them to the
475          ; terminal until a "$" is encountered. The "$" is not sent.
476 10CD E5          pstrng: push   h
477 10CE F5          push   psw
478 10D0 7E          serv4:  xchg    h,de ; hl=de
479 10D1 FE          24          serv4b: mov    a,m     ; a = byte from address hl
480 10D3 23          cpi    "$"
481 10D4 CA          10DE       jz    serv4a    ; exit without transmitting if character was '$'
482 10D7 5F          mov    e,a
483 10D8 CD          10BE       call   conout   ; send e to the terminal
484 10DB D0          jmp    serv4b
485 10DE EB          serv4a: xchg    h,de ; de = hl = pointer to character after the "$"
486 10DF E1          pop    h
487 10E0 F1          pop    psw
488 10E1 C9          ret
489
490          ; same as service 5 only it is a direct call
491 10E2 F5          displ6: push  psw
492 10E3 E5          push   h
493
494          ; Send the unsigned number in DE to the terminal as decimal.
495 10E4 D5          serv5:  push   d
496 10E5 C5          push   b
497 10E6 EB          xchg    h,de    ; hl= de
498 10E7 06          00          mwi    b,0      ; b=0 = number of digits
499 10E9 11          000A      lxi    d,10
500 10EC CD          114A      call   div16    ; hl=hl/10decimal
501 10EF 7B          mov    a,e      ; a= remainder (decimal digit)
502 10F0 C6          30          adi    '0'      ; make it an ascii digit
503 10F2 5F          mov    e,a
504 10F3 D5          push   d
505 10F4 04          inx    b        ; push the digit
506 10F5 7D          mov    a,l      ; l more digit on the stack
507 10F6 B4          ora    h
508 10F7 C2          10E9      jnz    serv5a   ; divide by ten again if hl<0
509
510 10FA D1          serv5c: pop    d
511 10FB CD          call   conout
512 10FE 05          dcr    b
513 10FF C2          10FA      jnz    serv5c
514 1102 C1          pop    b
515 1103 D1          pop    d
516 1104 E1          pop    h
517 1105 F1          pop    psw
518 1106 C9          ret
519
520          ; Send the signed number in DE to the terminal as decimal.
521 1107          serv6:  ; check for sign bit
522          ; if high, make de 2's complement
523          ; send out a '-'
524          ; call displ6
525
526 1107 7A          mov    a,d
527 1108 B7          ora    a
528 1109 F2          10E4      jp     serv5     ; display de in decimal if positive
529          ; de is negative, show sign and make 2's complement
530 110C D5          push   d
531 110D 2F          cma
532 110E 57          mov    d,a      ; complement d
533 110F 7B          mov    a,e
534 1110 2F          cma            ; complement a
535          ; while E is preserved, output the minus sign
536 1111 1E          2D          mvi    e,'-'    ; minus sign
537 1113 CD          10BE      call   conout   ; print '-'
538
539 1116 5F          mov    e,a      ; save complemented Accum.
540 1117 13          inx    l        ; 2's complement
541 1118 CD          10E2      call   displ6  ; display de in decimal
542 111B D1          pop    d        ; return original value of de
543 111C E1          pop    h
544 111D F1          pop    psw
545 111E C9          ret
546
547
548          ; This multiplies hl*de and returns the result in hl and de with hl being
549          ; the high word and de being the low word.
550 111F E1          serv7:  POP     H        ; hl = multiplicand, de = multiplier
551 1120 C5          PUSH    B
552 1121 44          MOV    B,H      ; save bc
553 1122 7D          MOV    A,L
554 1123 CD          1138      CALL   smpyx
555 1126 E5          PUSH    H
556 1127 67          MOV    H,A
557 1128 78          MOV    A,B
558 1129 44          MOV    B,H
559 112A CD          1138      CALL   smpyx
560 112D D1          POP    D
561 112E 4A          MOV    C,D
562 112F 09          DAD    B
563 1130 CE          00          ACI    0
564 1132 55          MOV    D,L
565 1133 6C          MOV    L,H
566 1134 67          MOV    H,A
567
568 1135 C1          POP    B        ; exit with result in hl:de
569 1136 F1          POP    PSW
570 1137 C9          RET
571
572 1138 21          smpyx:  LXI    H,0
573 113B 0E          MVI    C,8
574 113D 29          smpyx1: DAD    H
575 113E 17          RAL
576 113F D2          JNC    smpyx2
577 1142 19          DAD    D
578 1143 CE          00          ACI    0
579 1145 0D          smpyx2: DCR    C
580 1146 C2          JNZ    smpyx1
581 1149 C9          RET
582
583          ; This is the same as service 8
584 114A F5          div16:  push   psw
585 114B E5          push   h
586
587          ; Divide HL by DE and return the quotient in HL and remainder in DE
588 114C E1          serv8:  POP     H        ; GET THE NUMERATOR
589 114D C5          PUSH    B        ; SAVE USER'S BC
590 114E 42          MOV    B,D      ; PUT DE IN BC BECAUSE THE ORIGINAL VERSION OF
591 114F 4B          MOV    C,E      ; THIS PROGRAM HAD BC AS THE DIVISOR
592 1150 11          LXI    D,0
593 1153 EB          XCHG          ; DE = HIGH WORD AND HL = LOW WORD
594
595 1154 7B          MOV    A,E      ; SEE IF DENOMINATOR >= NUMERATOR
596 1155 91          SUB    C
597 1156 7A          MOV    A,D

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598 1157 98          SBB          B
599 1158 D2          JNC          SUSLA1
600                ; IF DENOMINATOR IS > NUMERATOR, QUOTIENT = 0 AND REMAINDER = NUMERATOR
601 115B EB          XCHG          ; HL= DE NUMERATOR
602 115C 11          LXI          D,0
603 115F C3          JMP          susla7
604 1162 3E          MVI          A,16
605 1164 29          DAD          H
606 1165 17          RAL
607 1166 EB          XCHG
608 1167 29          DAD          H
609 1168 D2          JNC          susla3
610 116B 13          INX          D
611 116C A7          ANA          A
612 116D EB          XCHG
613 116E 1F          RAR
614 116F 5F          PUSH          PSW
615 1170 D2          JNC          susla4
616 1173 7D          MOV          A,L
617 1174 91          SUB          C
618 1175 6F          MOV          L,A
619 1176 7C          MOV          A,H
620 1177 98          SBB          B
621 1178 67          MOV          H,A
622 1179 C3          JMP          susla5
623 117C 7D          MOV          A,L
624 117D 91          SUB          C
625 117E 6F          MOV          L,A
626 117F 7C          MOV          A,H
627 1180 98          SBB          B
628 1181 67          MOV          H,A
629 1182 D2          DAD          susla5
630 1185 09          DAD          B
631 1186 1B          DCX          D
632 1187 13          INX          D
633 1188 F1          POP          PSW
634 1189 3D          DCR          A
635 118A C2          JNZ          susla2
636
637 118D EB          susla7: XCHG          ; SWAP QUOTIENT AND REMAINDER
638 118E C1          POP          B
639 118F F1          POP          PSW
640 1190 C9          RET
641
642
643                ; ADCIN converts a voltage to a 6 bit number in L using successive approx.
644                ;
645 0009= settle equ 9                ; settling time for A/D circuit
646
647 1191 F5          adcin: push          psw
648 1192 E5          push          h
649
650 1193 C5          serv9: push          b                ; save bc
651 1194 0E          mvi          c,6                ; A/D resolution
652 1196 06          mvi          b,0010000b        ; l=mask
653 1198 2E          mvi          a,0                ; l=result
654 119A 78          mov          a,b                ; test=
655 119B B5          ora          l                ; result OR mask
656 119C 67          mov          h,a                ; save test
657 119D D3          out          portc            ; test->r,2r ladder (D/A)
658 119F B7          ora          a                ; rotate mask now...
659 11A0 78          mov          a,b                ; to allow for op-amp ...
660 11A1 1F          rar          ; slew (24uS) and ...
661 11A2 47          mov          b,a                ; transistor on/off (10 uS).
662 11A3 3E          mvi          a,settle         ; settling time
663 11A5 3D          adcin2: dcr          a
664 11A6 C2          jnz          adcin2           ; delay loop
665 11A9 20          rlm          ; get comparison
666 11AA B7          ora          a                ; l if input<D/A
667 11AB FA          jm          adcin3           ; if l result unchanged
668 11AE 6C          mov          l,h                ; else result = test
669 11AF C3          jmp          adcin4           ; see if done
670 11B2 3E          adcin3: mvi          a,0        ; this is dummy code...
671 11B4 00          nop          ; to balance out timing.
672 11B5 0D          dcr          c                ; dec # of bits
673 11B6 C2          jnz          adcin1          ; if not 0, new test
674 11B9 C1          pop          b                ; restore bc
675 11BA 7D          mov          a,l                ; a=A/D value
676 11BB E1          pop          h                ; restore hl
677 11BC 6F          mov          l,a                ; return result in l
678 11BD F1          pop          psw              ; restore psw
679 11BE C9          ret
680
681
682                ; returns L with the complemented value of the dip switch
683 11BF E1          servA: pop          h
684 11C0 DB          in          dip
685 11C2 2F          cma
686 11C3 6F          mov          l,a
687 11C4 F1          pop          psw
688 11C5 C9          ret
689
690                ; Waits for a key press and returns a value from the keypad in L
691 11C6 E1          servB: pop          h
692 11C7 C5          push          b
693 11C8 CD          call         rdkey           ; read the keypad
694 11CB 6F          mov          l,a
695 11CC C1          pop          b
696 11CD F1          pop          psw
697 11CE C9          ret
698
699                ; Writes the complement of E to portA
700 11CF E1          servC: pop          h
701 11D0 7B          mov          a,E
702 11D1 2F          cma
703 11D2 D3          out          porta
704 11D4 F1          pop          psw
705 11D5 C9          ret
706
707                ; This prints the hex value of DE to the terminal
708                ; Copyright 1990 Softaid Inc. modified by (ME)
709 11D6 D5          servD:
710 11D6 D5          HEX4: PUSH          D
711 11D7 EB          XCHG          ; HL = DE
712 11D8 7C          MOV          A,H
713 11D9 CD          CALL         HEX2           ; PRINT MSB
714 11DC 7D          MOV          A,L
715 11DD CD          CALL         HEX2           ; PRINT LSB
716 11E0 D1          POP          D
717 11E1 E1          POP          H
718 11E2 F1          POP          PSW
719 11E3 C9          RET
720
721 11E4 F5          HEX2: PUSH          psw        ; PRINT LSB
722 11E5 0F          RRC
723 11E6 0F          RRC
724 11E7 0F          RRC
725 11E8 0F          RRC
726 11E9 E6          ANI          0FH             ; UPPER 4 BITS
727 11EB CD          CALL         HXD
728 11EE F1          POP          PSW
729 11EF E6          ANI          0FH             ; LOWER 4 BITS
730
731                ;
732                ; CONVERT NIBBLE IN A TO ASCII AND DISPLAY IT
733 11F1 C6          90          HXD: ADI          90H           ; SET UP SO A-F MAKE CARRY
734 11F3 27          DAA
735 11F4 CE          ACI          40H
736 11F6 27          DAA
737 11F7 5F          MOV          E,A
738 11F8 CD          CALL         CONOUT         ; Display digit
739 11FB C9          RET
740
741                ; DACout generates a voltage from the low 6 bits of E
742 11FC 7B          serve: mov          a,E
743 11FD E6          ani          11111b         ; mask off all but lower 6 bits
744 11FF D3          out          portc
745 1201 E1          pop          h
746 1202 F1          pop          psw

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747 1203 C9          ret
748
749 1204 E1          servF:  pop    h
750 1205 F1          pop    psw
751 1206 C9          ret
752
753                ; This sets the frequency of the speaker timer according to the value
754                ; of DE and turns on the speaker.  If de = 0 then the speaker is turned off
755 1207 D5          serv10:  push   d
756 1208 C5          push   b          ; buzzon and buzzoff use B
757 1209 EB          xchg   b          ; hl = de
758 120A CD          call   sdiv         ; set the new frequency
759 120D 7A          mov    a,d
760 120E B3          ora    e
761 120F F5          push   psw         ; save z flag
762 1210 C4          cnz    buzzon
763 1213 F1          pop    psw         ; restore z flag
764 1214 CC          0154   cz    buzzoff
765 1217 C1          pop    b
766 1218 D1          pop    d
767 1219 E1          pop    h
768 121A F1          pop    psw
769 121B C9          ret
770
771                ; This sends the bit pattern in E to the LED display number in D.
772                ; The leftmost display is 5 and the rightmost is 0
773 121C 3E          80     serv11:  mvi    a,rgtdsp
774 121E 82          add    d
775 121F FE          86     cpi    rgtdsp+6
776 1221 D2          1229   jnc    serv11a    ; if > rgtdsp+6 we are out of range
777 1224 D3          41     out    dspcmd     ; select display
778 1226 7B          mov    a,e        ; a = bit pattern
779 1227 D3          40     out    dspout     ; output bit pattern to the display
780 1229 E1          serv11a: pop    h
781 122A F1          pop    psw
782 122B C9          ret
783
784                ; display de in the 4 leftmost digits as hex
785 122C D5          serv12:  push   d
786 122D EB          xchg   b          ; hl=de
787 122E C5          push   b
788 122F CD          011D   call   daddr
789 1232 C1          pop    b
790 1233 D1          pop    d
791 1234 E1          pop    h
792 1235 F1          pop    psw
793 1236 C9          ret
794
795                ; display de in the 4 leftmost digits as decimal (9999 is the max)
796 1237 C5          serv13:  push   d
797 1238 D5          push   d
798 1239 CD          1765   call   bin2bcd
799 123C EB          xchg   b
800 123D CD          011D   call   daddr     ; print the BCD value of hl
801 1240 D1          pop    d
802 1241 C1          pop    b
803 1242 E1          pop    h
804 1243 F1          pop    psw
805 1244 C9          ret
806
807
808                ; Delay according to the value of hl
809
810 1245 E1          serv14:  pop    h
811 1246 E5          push   h
812 1247 CD          0181   call   delay
813 124A E1          pop    h
814 124B F1          pop    psw
815 124C C9          ret
816
817
818                ; return the complement of input port B, (same as DIPSWIN)
819 124D C3          11BF   serv15:  jmp    servA
820
821                ; KEYSTAT: If no key pressed HL is returned as 0.  If key pressed
822                ; H is returned as 1 and L with the value of the key
823 1250 E1          serv16:  pop    h
824 1251 C5          push   b
825 1252 CD          0129   call   -1 plkpad  ; this returns FF if no key
826 1255 FE          cpi    -1
827 1257 21          0000   lki    h,0        ; assume no key
828 125A CA          125F   jz     sv16ex     ; exit if no key
829 125D 24          inr    h          ; make h=1
830 125E 6F          mov    l,a        ; l = key
831 125F C1          sv16ex: pop    b
832 1260 F1          pop    psw
833 1261 C9          ret
834
835                ; DIGOUT: Display the hex digit in E on display #D
836 1262 C5          serv17:  push   b
837 1263 7A          mov    a,D        ; put display number in A
838 1264 C6          ad    rgtdsp     ; offset from the right display
839 1266 47          mov    b,a        ; B points to the display
840 1267 7B          mov    a,E        ; A is the digit
841 1268 CD          00F7   call   digout
842 126B C1          pop    b
843 126C E1          pop    d
844 126D F1          pop    h
845 126E C9          ret
846
847                ; write to RTC
848
849 126F E1          serv18:  POP    H
850 1270 F1          POP    PSW
851
852                ;
853                ; Write to the SMARTCLOCK
854                ; This must be done in two passes because registers are used to load the data
855                ; since the RAM is the only bank available
856                ; The DE register must point to 1st of 8 bytes to write to RTC.
857 1271 F5          WRSC1:  PUSH   PSW
858 1272 C5          PUSH   B
859 1273 D5          PUSH   D
860 1274 E5          PUSH   H
861 1275 20          RIM
862 1276 FE          PUSH   PSW     ; SAVE IE STATUS
863 1277 D5          PUSH   D
864 1278 CD          136E   CALL   CLOCKOFF ; SHUT OFF THE CLOCK WHILE WRITING TO IT
865 127B D1          POP    D
866 127C CD          135E   CALL   LHLBC    ; LOAD HL AND BC WITH RTC DATA
867 127F D5          PUSH   D
868 1280 16          41     MVI    D,65
869 1282 3A          FFF5   WRSCS:  LDA    CLKDUM
870 1285 15          DCR    D
871 1286 C2          1282   JNZ    WRSCS
872
873                ; SEND PATTERN RECOGNITION SEQUENCE
874 1289 16          04     MVI    D,4        ; NUMBER OF SWAP/COMPLEMENT OPERATIONS
875 128B 3E          5C     MVI    A,5CH     ; INITIAL VALUE WILL BE CHANGED TO 5C
876 128D 0F          WRCL:  RRC
877 128E 0F          RRC
878 128F 0F          RRC
879 1290 0F          RRC
880 1291 1E          08     WRCS:  MVI    E,8
881 1293 32          FFF5   WRSC0:  STA    CLKDUM
882 1296 0F          RRC
883 1297 1D          DCR    E
884 1298 C2          1293   JNZ    WRSC0
885 129B 87          ORA    A          ; SEE IF BIT 7 IS 0
886 129C F2          JP    WRC2       ; JUMP IF POSITIVE
887 129F 2F          CMA          ; NOW COMPLEMENT
888 12A0 C3          1291   JMP    WRC3       ; SEND THE COMPLEMENT
889 12A3 15          WRCS:  DCR    D          ; LOOP 4 TIMES
890 12A4 C2          128D   JNZ    WRC1
891
892                ; THE PATTERN HAS BEEN SENT SO NOW STORE THE DATA IN THE CLOCK
893 12A7 16          08     WRSPAT: MVI    D,8
894 12A9 79          MOV    A,C        ; STORE HUNDREDS
895 12AA 32          FFF5   WRSC2:  STA    CLKDUM

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896 12AD 0F          RRC
897 12AE 15          DCR
898 12AF C2         12AA  JNZ      WRSC2
899
900 12B2 16         08      MVI      D,8
901 12B4 78          MOV      A,B      ; STORE SEC
902 12B5 32         FFF5    WRSC3:   STA      CLKDUM
903 12B8 0F          RRC
904 12B9 15          DCR
905 12BA C2         12B5    JNZ      WRSC3
906
907 12BD 16         08      MVI      D,8
908 12BF 7D          MOV      A,L      ; STORE MIN
909 12C0 32         FFF5    WRSC4:   STA      CLKDUM
910 12C3 0F          RRC
911 12C4 15          DCR
912 12C5 C2         12C0    JNZ      WRSC4
913
914 12C8 16         08      MVI      D,8
915 12CA 7C          MOV      A,H      ; STORE HOUR
916 12CB 32         FFF5    WRSC5:   STA      CLKDUM
917 12CE 0F          RRC
918 12CF 15          DCR
919 12D0 C2         12CB    JNZ      WRSC5
920
          ; IGNORE THE REST OF THE VARIABLES ON THIS PASS
921
922 12D3 06         20      MVI      B,8*4
923 12D5 3A         FFF5    WRSC7:   LDA      CLKDUM
924 12D8 05          DCR      B
925 12D9 C2         12D5    JNZ      WRSC7
926
          ; **PASS 2**
927
928 12DC D1         135E   POP      D
929 12DD CD         CALL     LHLBC      ; LOAD HL AND BC WITH RTC DATA
930                                     ; C = DAY, B= DATE, H = YEAR, L= MONTH
931
932 12E0 16         41      MVI      D,65
933 12E2 3A         FFF5    WRSCSA:  LDA      CLKDUM
934 12E5 15          DCR      D
935 12E6 C2         12E2    JNZ      WRSCSA
936
          ; SEND PATTERN RECOGNITION SEQUENCE
937
938 12E9 16         04      MVI      D,4      ; NUMBER OF SWAP/COMPLEMENT OPERATIONS
939 12EB 3E         5C      MVI      A,5CH
940 12ED 0F          RRC
941 12EE 0F          RRC
942 12EF 0F          RRC
943 12F0 0F          RRC      ; SWAP NIBBLES
944 12F1 1E         08      WRC3A:  MVI      E,8
945 12F3 32         FFF5    WRSC0A: STA      CLKDUM
946 12F6 0F          RRC
947 12F7 1D         12F3    DCR      E
948 12F8 C2         12F3    JNZ      WRSC0A
949 12FB B7          ORA      A      ; SEE IF BIT 7 IS 0
950 12FC F2          JP       WRC2A   ; JUMP IF POSITIVE
951 12FD 2F          CMA      ; NOW COMPLEMENT
952 1300 C3         12F1    JMP      WRC3A   ; SEND THE COMPLEMENT
953 1303 15          DCR      D      ; LOOP 4 TIMES
954 1304 C2         12ED    JNZ      WRC1A
955
          ; SKIP THE FIRST 4 REGISTERS
956
957
958 1307 16         20      WRC2A:  MVI      D,8*4
959 1309 3A         FFF5    WRSC11: LDA      CLKDUM
960 130C 15          DCR      D
961 130D C2         1309    JNZ      WRSC11
962
          ; LOAD THE CLOCK REGISTERS, DAY-DATE-MONTH-YEAR
963
964 1310 16         08      MVI      D,8
965 1312 79          MOV      A,C      ; STORE DAY
966 1313 F6         30      ORI      110000B  ; DON'T TURN ON CLOCK YET
967 1315 32         FFF5    WRSC8:  STA      CLKDUM
968 1318 0F          RRC
969 1319 15          DCR
970 131A C2         1315    JNZ      WRSC8
971
972 131D 16         08      MVI      D,8
973 131F 78          MOV      A,B      ; DATE
974 1320 32         FFF5    WRSC9:  STA      CLKDUM
975 1323 0F          RRC
976 1324 15          DCR
977 1325 C2         1320    JNZ      WRSC9
978
979 1328 0F         08      MVI      B,8
980 132A 7D          MOV      A,L      ; STORE MONTH
981 132B 32         FFF5    WRSC10: STA      CLKDUM
982 132E 0F          RRC
983 132F 05          DCR
984 1330 C2         132B    JNZ      WRSC10
985
986 1333 06         08      MVI      B,8
987 1335 7C          MOV      A,H      ; STORE YEAR
988 1336 32         FFF5    WRSC14: STA      CLKDUM
989 1339 0F          RRC
990 133A C5          DCR
991 133B C2         1336    JNZ      WRSC14
992
993 133E 79          MOV      A,C      ; CHECK OSC BIT
994 133F E6         20      ANI      100000B
995 1341 C2         1347    JNZ      WRSC15  ; SKIP IF OSC BIT SET
996 1344 CD         1369    CALL     CLOCKON  ; TURN ON THE CLOCK
997 1347 F1         08      POP      PSW
998 1348 E6         08      ANI      1000B   ; CHECK IE STATUS
999 134A E1         POP      H
1000 134B D1        POP      D
1001 134C C1        POP      B
1002 134D C2        1352    JNZ      WRSC12  ; JMP IF EI
1003 1350 F1        POP      PSW
1004 1351 C9        RET
1005 1352 F1        WRSC12: POP      PSW
1006 1353 FB        EI
1007 1354 C9        RET
1008
1009
1010
1011 1355 C5        3A A3 5C C5 3A  ;
          CLKTBL: DB 0C5H,03AH,0A3H,05CH,0C5H,03AH,0A3H,05CH,0      ; A ZERO ADDED AT END
1012
1013
          ; This loads HL BC with the data pointed to by DE and returns DE = DE + 4
1014 135E EB        LHLBC:  XCHG
1015 135F 4E        MOV      C,M      ; HL POINTS TO RTC DATA
1016 1360 23        MOV      INX      ; C= HUNDREDS
1017 1361 46        MOV      B,M      ; B= SECONDS
1018 1362 23        MOV      INX      H
1019 1363 5E        MOV      E,M
1020 1364 23        MOV      INX      H
1021 1365 56        MOV      D,M      ; D = HOURS, E = MINUTES
1022 1366 23        MOV      INX      H
1023 1367 EB        XCHG      ; DE = POINTER TO RTC DATA, HL = HRS, MIN
1024 1368 C9        RET
1025
1026
          ; Turn on the real time clock
1027
1028 1369 06        00      clockon: MVI      B,0
1029 136B C3        1370    JMP      clk00
1030
          ; Turn off the real time clock
1031
1032 136E 06        01      clockoff: MVI     B,1
1033
1034 1370          clk00:
1035 1370 16        41      CLK5:   MVI      D,65
1036 1372 3A        FFF5    LDA      CLKDUM
1037 1375 15        DCR      D
1038 1376 C2        1372    JNZ      CLK5
1039
          ; SEND OUT THE SMARTWATCH COMPARISON PATTERN
1040
1041 1379 21        1355   LXI      H,CLKTBL
1042 137C 1E        08      MVI      E,8
1043 137E 16        08      clk0:  MVI      D,8
1044 1380 7E        MOV      A,(HL)

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1045 1381 32      FFF5      clk1:      STA      CLKDUM
1046 1384 0F      RRC
1047 1385 15      DCR
1048 1386 C2      1381      JNZ      clk1
1049 1389 23      INX      H
1050 138A 1D      DCR      E
1051 138B C2      137E      JNZ      clk0
1052
1053              ; This writes to the OSC bit within the DAY register
1054 138E 16      MVI      D,8*4 + 4      ; SKIP 4 REGS AND 4 BITS TO GET ON/OFF BIT
1055 1390 3A      FFF5      CLK3:      LDA      CLKDUM      ; READ THE BIT
1056 1393 15      DCR      D
1057 1394 C2      1390      JNZ      CLK3
1058
1059 1397 3E      MVI      A,1
1060 1399 32      FFF5      STA      CLKDUM      ; SET THE RST BIT
1061 139C 78      MOV      A,B          ; GET ON/OFF FLAG FROM B REGISTER
1062 139D 32      FFF5      STA      CLKDUM      ; AND THE OSC BIT
1063 13A0 16      1A      MVI      D,8*3 + 2      ; SKIP 2 BITS OF DAY AND 3 REGS
1064 13A2 3A      FFF5      CLK4:      LDA      CLKDUM      ; READ THE BIT
1065 13A5 15      DCR      D
1066 13A6 C2      13A2      JNZ      CLK4
1067 13A9 C9      RET
1068
1069
1070              ; READ RTC
1071 13AA E1      serv19:   POP      H
1072 13AB F1      POP      PSW
1073              ; The DE register must be a pointer to an array to store the data from RTC
1074 13AC F5      RDSClk:  PUSH     PSW
1075 13AD C5      PUSH     B
1076 13AE D5      PUSH     D
1077 13AF E5      PUSH     H
1078 13B0 20      RIM
1079 13B1 F3      DI
1080 13B2 F5      PUSH     PSW      ; SAVE EI STATUS
1081 13B3 D5      PUSH     D          ; SAVE POINTER TO RTC ARRAY
1082
1083              ; We will use the MOS register storage to store the SP reg since it
1084              ; is only needed when single stepping.
1085 13B4 21      0000      LXI      H,0          ; SAVE THE STACK POINTER
1086 13B7 39      DAD      SP          ; BECAUSE WE NEED ALL THE
1087 13B8 22      FFF3      SHLD     SPREG      ; REGISTERS WE CAN GET
1088
1089 13BB 16      41      RDSCS:   MVI      D,65
1090 13BD 3A      FFF5      LDA      CLKDUM
1091 13C0 15      DCR      D
1092 13C1 C2      13BD      JNZ      RDSCS
1093
1094              ; SEND OUT THE SMARTWATCH COMPARISON PATTERN
1095 13C4 21      1355      LXI      H,CLKTBL
1096 13C7 1E      08      MVI      E,8
1097 13C9 16      08      RDSC0:   MVI      D,8
1098 13CB 7E      MOV      A,M
1099 13CC 32      FFF5      RDSC1:   STA      CLKDUM
1100 13CF 0F      RRC
1101 13D0 15      DCR      D
1102 13D1 C2      13CC      JNZ      RDSC1
1103 13D4 23      INX      H
1104 13D5 1D      DCR      E
1105 13D6 C2      13C9      JNZ      RDSC0
1106
1107              ;THE 64 BIT PATTERN HAS BEEN SENT, SO NOW READ THE 64 BITS OF DATA
1108              ; FIRST IS HUNDREDS THEN SEC, MIN, HOUR, DAY, DATE, MONTH AND YEAR
1109 13D9 16      08      MVI      D,8
1110 13DB 1E      00      MVI      E,0
1111 13DD 3A      FFF5      RDSC3:   LDA      CLKDUM
1112 13E0 1F      RAR
1113 13E1 7B      MOV      A,E
1114 13E2 1F      RAR
1115 13E3 5F      MOV      E,A      ; E IS THE HUNDREDS
1116 13E4 15      DCR      D
1117 13E5 C2      13DD      JNZ      RDSC3
1118
1119 13E8 16      08      MVI      D,8
1120 13EA 0E      00      MVI      C,0
1121 13EC 3A      FFF5      RDSC4:   LDA      CLKDUM
1122 13EF 1F      RAR
1123 13F0 79      MOV      A,C
1124 13F1 1F      RAR
1125 13F2 4F      MOV      C,A
1126 13F3 15      DCR      D
1127 13F4 C2      13EC      JNZ      RDSC4      ; C IS THE SECONDS
1128
1129 13F7 16      08      MVI      D,8
1130 13F9 06      00      MVI      B,0
1131 13FB 3A      FFF5      RDSC5:   LDA      CLKDUM
1132 13FE 1F      RAR
1133 13FF 78      MOV      A,B
1134 1400 1F      RAR
1135 1401 47      MOV      B,A
1136 1402 15      DCR      D
1137 1403 C2      13FB      JNZ      RDSC5      ; B = MINUTES
1138
1139 1406 16      08      MVI      D,8
1140 1408 2E      00      MVI      L,0
1141 140A 3A      FFF5      RDSC6:   LDA      CLKDUM
1142 140D 1F      RAR
1143 140E 7D      MOV      A,L
1144 140F 1F      RAR
1145 1410 6F      MOV      L,A
1146 1411 15      DCR      D
1147 1412 C2      140A      JNZ      RDSC6      ; THIS WILL BE PUT IN SP LATER
1148
1149 1415 16      08      MVI      D,8
1150 1417 26      00      MVI      H,0
1151 1419 3A      FFF5      RDSC7:   LDA      CLKDUM
1152 141C 1F      RAR
1153 141D 7C      MOV      A,H
1154 141E 1F      RAR
1155 141F 67      MOV      H,A
1156 1420 15      DCR      D
1157 1421 C2      1419      JNZ      RDSC7
1158 1424 F9      SPHL
1159              ; SP HOLDS HOUR AND DAY
1160 1425 16      08      MVI      D,8
1161 1427 2E      00      MVI      L,0
1162 1429 3A      FFF5      RDSC8:   LDA      CLKDUM
1163 142C 1F      RAR
1164 142D 7D      MOV      A,L
1165 142E 1F      RAR
1166 142F 6F      MOV      L,A
1167 1430 15      DCR      D
1168 1431 C2      1429      JNZ      RDSC8      ; L IS THE DATE
1169
1170 1434 16      08      MVI      D,8
1171 1436 26      00      MVI      H,0
1172 1438 3A      FFF5      RDSC9:   LDA      CLKDUM
1173 143B 1F      RAR
1174 143C 7C      MOV      A,H
1175 143D 1F      RAR
1176 143E 67      MOV      H,A
1177 143F 15      DCR      D
1178 1440 C2      1438      JNZ      RDSC9      ; H IS THE MONTH
1179
1180              ; Since the upper 3 bits of month are not used, this will be the
1181              ; counter for the loop
1182 1443 16      00      MVI      D,0
1183 1445 3A      FFF5      RDSC10:  LDA      CLKDUM
1184 1448 1F      RAR
1185 1449 7A      MOV      A,D
1186 144A 1F      RAR
1187 144B 57      MOV      D,A
1188 144C 7C      MOV      A,H
1189 144D C6      ADI      00100000B      ; INCREMENT THE UPPER 3 BIT COUNTER
1190 144F 67      MOV      H,A          ; SAVE COUNTER VALUE
1191 1450 D2      1445      JNC      RDSC10
1192              ; UPPER 3 BITS OF H ARE ZERO AGAIN (A=H)
1193

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1194 1453 7A          MOV      A,D          ; D IS THE YEAR
1195 1454 32          STA      AFREG+7
1196 1457 22          SHLD   AFREG+5      ; STORE MONTH AND DATE
1197 145A 21          LXI    H,0
1198 145D 39          DAD    SP
1199 145E 22          FFEE          SHLD   AFREG+3      ; STORE DAY AND HOUR
1200 1461 60          MOV    H,B
1201 1462 69          MOV    L,C
1202 1463 22          FFEC          SHLD   AFREG+1      ; STORE MIN AND SEC
1203 1466 78          MOV    A,E
1204 1467 3A          FFEB          STA      AFREG      ; STORE HUNDREDS
1205
1206 146A 2A          FFF3          LHLD   SPREG        ; GET SP
1207 146D F9          SPHL
1208 146E D1          POP    D            ; RESTORE SP
1209 146F 21          FFEB          LXI    H,AFREG      ; DE POINTS TO ARRAY TO PUT CLOCK DATA
1210 1472 06          08          MVI    B,8          ; HL POINTS TO DATA THAT WAS JUST STORED
1211 1474 7E          RDSCL1:      MOV    A,M          ; READ IT
1212 1475 12          STAX   D            ; AND STORE IT
1213 1476 23          INX    H
1214 1477 13          INX    D
1215 1478 05          DCR    B
1216 1479 C2          1474        JNZ    RDSC11
1217 147C F1          POP    PSW
1218 147D E6          08          ANI    1000B       ; CHECK IE STATUS
1219 147F E1          POP    H
1220 1480 D1          POP    D
1221 1481 C1          POP    B
1222 1482 CA          1488        JZ     RDSC12      ; JMP IF INTERRUPTS WERE NOT ENABLED
1223 1485 F1          POP    PSW
1224 1486 FB          EI
1225 1487 C9          RET
1226 1488 F1          RDSCL2:      POP    PSW
1227 1489 C9          RET
1228
1229
1230
1231
1232          ;
1233          ; Output E bytes of the string of bit patterns pointed to by HL, starting
1234          ; at display D and counting down.
1235          ;
1236          LEDSTR:  push   psw
1237          148B      push   h
1238          serv1A:  pop    h          ; get HL
1239          148D      push   h          ; put it back
1240          148E      push   b
1241          148F      mov    a,d          ; a = display number (rightmost = 0)
1242          1490      adi    rgtdsp      ; offset by rgtdsp
1243          1491      mov    b,a
1244          1492      mov    c,e          ; e = # of bytes to display
1245          1493      call   funl      ; display string of bit patterns
1246          1494      call   funl
1247          1495      pop    b
1248          1496      pop    h
1249          1497      pop    psw
1250          1498      ret
1251
1252          ;
1253          ; Display the hex byte in E on the two displays on the right
1254          ;
1255          149B      serv1B:  push   b
1256          149C      mov    a,e
1257          149D      call   ddata
1258          14A0      pop    b
1259          14A1      pop    h
1260          14A2      pop    psw
1261          14A3      ret
1262
1263          ;
1264          ;
1265          ; EPROM BURNING CODE
1266          ;
1267          00E0=    bnpорта equ    0e0h      ; EPROM board
1268          00E1=    bnpортb equ    0e1h
1269          00E2=    bnpортc equ    0e2h
1270          00E3=    bncntrl equ    0e3h
1271          00E4=    bnpортd equ    0e4h
1272
1273          ;
1274          ;
1275          ; Delays needed to program EPROM
1276          ;
1277          14A4      DLAYA:  PUSH   PSW          ; approx 5ms (6ms)
1278          14A5      PUSH   H
1279          14A6      LXI    H,641      ; DELAY FOR 3.072 MHZ CLOCK
1280          14A9      C3          14B9        JMP    DLAY2
1281
1282          14AC      DLAYB:  PUSH   PSW          ; approx 2.5ms (3ms)
1283          14AD      PUSH   H
1284          14AE      LXI    H,320
1285          14B1      C3          14B9        JMP    DLAY2
1286
1287          14B4      DLAYC:  PUSH   PSW          ; approx 1.25ms (1.5)
1288          14B5      PUSH   H
1289          14B6      LXI    H,160
1290
1291          ;
1292          ; TIME delay          ; for 8085 is 24 t states
1293          14B9      2B          DLAY2:  DCX    H          ; 6 T STATES
1294          14BA      7C          MOV    A,H          ; 4 T STATES
1295          14BB      B5          ORA    L            ; 4 T STATES
1296          14BC      C2          14B9        JNZ    DLAY2      ; 10 T STATES
1297          14BF      E1          POP    H
1298          14C0      F1          POP    PSW
1299          14C1      C9          RET
1300
1301          ;
1302          ; settling time for relays
1303          ;
1304          14C2      CD          14A4        rlyst1: call   dlaya
1305          14C5      CD          14A4        call   dlaya
1306          14C8      CD          14A4        call   dlaya
1307          14CB      C9          ret
1308
1309          ;
1310          ; TURN OFF EPROM PROGRAMMER. Turn off main power, then the control relays.
1311          ;
1312          14CC      F5          eoff:  push   PSW
1313          14CD      3E          MVI    a,80h
1314          14CF      E3          out    bncntrl      ; set port C as output
1315          14D1      3E          FF     MVI    a,0ffh
1316          14D3      D3          E2          out    bnpорта      ; write FF to input of EPROM in case of WR
1317          14D5      7D          MOV    a,l          ; type byte from TYPECHK
1318          14D6      E6          7F          ani    01111111b   ; disable main power
1319          14D8      D3          E4          out    bnpортd
1320          14DA      CD          14C2        rlyst1: call   rlyst1
1321          14DD      AF          XRA    a
1322          14DE      D3          E4          out    bnpортd      ; now shut off control relays
1323          14E0      CD          14C2        call   rlyst1
1324          14E3      CD          14E8        call   bncnt      ; make port C input again
1325          14E6      F1          pop    psw
1326          14E7      C9          ret
1327
1328          14E8      3E          89          bncnt:  MVI    a,89h
1329          14EA      D3          E3          out    bncntrl
1330          14EC      C9          ret
1331
1332          ;
1333          ;
1334          ; Send DE to the address lines of EPROM programmer
1335          ;
1336          14ED      F5          OUTAD:  PUSH   PSW
1337          14EE      7B          MOV    A,E
1338          14EF      D3          E0          OUT    BNPОРТА
1339          14F1      7A          MOV    A,D
1340          14F2      D3          E1          OUT    BNPОРТB
1341          14F4      F1          POP    PSW
1342          14F5      C9          RET

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1343
1344
1345
1346
1347
1348
1349 14F6 CD 14ED RDEPR: CALL OUTAD
1350 14F9 2C INR L
1351 14FA 7D MOV A,L
1352 14FB D3 OUT BNPORTRD
1353 14FD DB E2 IN BNPORTRC ; READ EPROM BYTE
1354 14FF F5 PUSH PSW
1355 1500 2D DCR L
1356 1501 7D MOV A,L
1357 1502 D3 OUT BNPORTRD
1358 1504 F1 POP PSW
1359 1505 C9 RET
1360
1361
1362 ; GARG:
1363 ; Put the arguments from the registers on the stack
1364 ; H= page of RAM
1365 ; L= page of EPROM
1366 ; DE= # of bytes
1367 ; B= EPROM type
1368
1369 1506 78 garg: mov a,b
1370 1507 C1 pop b ; get ret address
1371 1508 E5 push h
1372 1509 2E 00 mvi l,0 ; make RAM address
1373 150B E3 xthl ; put on stack and restore H
1374 150C 65 mov h,l ; put in hi byte
1375 150D 2E 00 mvi l,0 ; clear low byte
1376 150F E5 push h ; push EPROM address
1377 1510 D5 push d ; push # of bytes
1378 1511 26 00 mvi h,0
1379 1513 6F mov l,a ; HL = type
1380 1514 E5 push h ; push type
1381 1515 C5 push b ; save ret addr
1382 1516 C9 ret
1383
1384
1385
1386 ;
1387 ; The selected EPROM type is on top of stack. The routine POPs this and
1388 ; pushes second byte of the byte pairs in table E512 on stack
1389 ; cy = 0 if invalid type
1390 1517 D1 TYPECHK: POP D ; DE= RET ADDRESS
1391 1518 E1 POP H ; GET TYPE BYTE
1392 1519 7D MOV A,L
1393 151A 3D DCR A ; CHANGE 1-6 TO 0-5
1394 151B FE 06 CPI 6
1395 151D D2 1536 JNC argerr ; IF A>=5 THEN THERE IS A TYPE ERROR
1396 1520 21 153E LXI H,E512
1397 1523 07 RLC
1398 1524 85 ADD L
1399 1525 6F MOV L,A
1400 1526 D2 152A JNC NOINC
1401 1529 24 INR H
1402 152A 7E E4 MOV A,M
1403 152B D3 E4 OUT BNPORTRD
1404 152D CD 14C2 call rlystl
1405 1530 23 INX H
1406 1531 6E MOV L,M ; GET BYTE FROM THE TABLE
1407 1532 7D MOV A,L
1408 1533 D3 E4 OUT BNPORTRD
1409 1535 37 STC ; SET CY = NO ERROR
1410 1536 26 MVI H,0
1411 1538 E5 00 PUSH H ; TYPE IS NOW A BYTE FROM TABLE E512
1412 1539 CD 14C2 CALL rlystl ; settle the relays
1413 153C D5 PUSH D ; PUSH RET ADDRESS
1414 153D C9 RET
1415
1416 ; EPROM BOARD CONTROL BYTES
1417 153E 30 B0 E512: DEFB 30H,0B0H ;TYPE 1 27512 EPROM
1418 1540 10 90 DEFB 10H,090H ;TYPE 2 27256 EPROM
1419 1542 80 80 DEFB 80H,080H ;TYPE 3 27128 EPROM 12.5 VOLT
1420 1544 40 C0 DEFB 40H,0C0H ;TYPE 4 27128 EPROM 21 VOLT
1421 1546 80 80 DEFB 80H,080H ;TYPE 5 2764 EPROM 12.5 VOLT
1422 1548 40 C0 DEFB 40H,0C0H ;TYPE 6 2764 EPROM 21 VOLT
1423
1424 0011= numbrn: equ 17 ; 50ms / 3ms (max ms)/(delay ms)
1425
1426 ;
1427 ; SMOKE BURNS THE BYTE IN THE ACCUMULATOR
1428 ;
1429 154A F5 SMOKE: PUSH PSW
1430 154B 3E 80 MVI A,80H
1431 154D D3 E3 OUT BNCNTRL
1432 154F CD 14ED CALL OUTAD
1433 1552 F1 POP PSW ; GET A
1434 1553 F5 PUSH PSW ; SAVE IT AGAIN
1435 1554 D3 E2 OUT BNPORTRC ; SEND A TO PORT C (DATA)
1436 1556 2C INR L
1437 1557 2C INR L ; turn WE on
1438 1558 7D MOV A,L
1439 1559 D3 E4 OUT BNPORTRD
1440 155B CD 14AC CALL DELAYB ; 2.5ms 8/20/91
1441 155E 2D DCR L
1442 155F 2D DCR L ; turn WE off
1443 1560 7D MOV A,L
1444 1561 D3 E4 OUT BNPORTRD
1445 1563 3E 89 MVI A,89H
1446 1565 D3 E3 OUT BNCNTRL ; CONTROL PORT
1447 1567 CD 14ED CALL OUTAD
1448 156A F1 POP PSW
1449 156B C9 RET
1450
1451 ;
1452 ; Called by BRNIT. Burns the byte in A and returns z=false if okay
1453 ; and Z=true if burn error.
1454 ;
1455 156C D5 BRN: PUSH D
1456 156D C5 PUSH B
1457 156E 0E 11 MVI C,NUMBRN ; see DELAY
1458 1570 CD 154A BRN0: CALL SMOKE
1459 1573 47 MOV B,A ; SAVE A
1460 1574 2C INR L
1461 1575 7D MOV A,L
1462 1576 D3 E4 OUT BNPORTRD ; OUTPUT ENABLE
1463 1578 DB E2 IN BNPORTRC ; READ WHAT WAS WRITTEN AT BNPORTRC (DATA)
1464 157A 67 MOV H,A
1465 157B 2D DCR L
1466 157C 7D MOV A,L
1467 157D D3 E4 OUT BNPORTRD ; DISABLE OUTPUT
1468 157F 78 MOV A,B ; RESTORE BURN BYTE
1469 1580 BC CMB H
1470 1581 CA 158D JZ BRN1 ; IF IT BURNED CORRECTLY, SKIP BELOW
1471 1584 0D DCR C
1472 1585 C2 1570 JNZ BRN0 ; BURN IT AGAIN (BUT ONLY TOTAL OF 50ms)
1473 1588 7C MOV L,A ; L = RAM BYTE
1474 1589 6F MOV A,H ; A = EPROM BYTE
1475 158A C1 POP B
1476 158B D1 POP D ; Z=TRUE = BURN PROBLEM
1477 158C C9 RET
1478
1479 158D 78 BRN1: MOV A,B ; RESTORE BURN BYTE
1480 158E CD 154A CALL SMOKE ; DO IT AGAIN TO BE SURE
1481 1591 3E 01 MVI A,1
1482 1593 B7 ORA A ; Z=0 = BURNT OKAY
1483 1594 C1 POP B
1484 1595 D1 POP D
1485 1596 C9 RET
1486
1487 ; MAIN EPROM BURNING SUBROUTINE
1488 ; Z=FALSE IF NO ERROR, TRUE OTHERWISE
1489 ;
1490 1597 E1 BRNIT: POP H ; GET RET ADDRESS
1491 1598 C1 POP B ; BC = TYPE BYTE

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1492 1599 79
1493 159A C1
1494 159B D1
1495 159C E3
1496 159D C5
1497 159E 44
1498 159F 4D
1499 15A0 6F
1500 15A1 CD 14ED
1501 15A4 2C
1502 15A5 7D
1503 15A6 D3 E4
1504 15A8 DB E2
1505 15AA 67
1506 15AB 2B
1507 15AC 7D
1508 15AD D3 E4
1509 15AF 0A
1510 15B0 BC
1511 15B1 CA 15C2
1512 15B4 CD 156C
1513 15B7 C2 15C2
1514 15BA CD 14CC
1515 15BD E1
1516 15BE 6F
1517 15BF 0A
1518 15C0 67
1519
1520 ; BC = BAD RAM ADDRESS
1521 ; DE = BAD ROM ADDRESS
1522 ; H = RAM BYTE
1523 ; L = ROM BYTE
1524
1525 15C1 C9
1526
1527 15C2 03
1528 15C3 13
1529 15C4 E3
1530 15C5 E5
1531 15C6 C5
1532 15C7 EB
1533 15C8 CD 011D
1534 15CB EB
1535 15CC C1
1536 15CD E1
1537
1538 15CE 2B
1539 15CF 7D
1540 15D0 B4
1541 15D1 E3
1542 15D2 C2 15A1
1543 15D5 CD 14CC
1544 15D8 F1
1545 15D9 3E 01
1546 15DB B7
1547 15DC C9
1548
1549 ;
1550 ; UPON ENTRY TOP OF STACK = TYPE FROM TYPECHK FOLLOWED BY ADDRESS
1551 ; TO CHECK FOR ERASED EPROM.
1552 ;
1553 15DD C1
1554 15DE E1
1555 15DF D1
1556 15E0 C5
1557 15E1 13
1558 15E2 26 FF
1559
1560 15E4 1B
1561 15E5 CD 14F6
1562 15E8 BC
1563 15E9 C2 15F1
1564 15EC 7A
1565 15ED B3
1566 15EE C2 15E4
1567
1568 15F1 CD 14CC
1569 15F4 C9
1570
1571 ;
1572 ; Stack must have on top: the type from TYPECHK, the number of bytes to check,
1573 ; the EPROM address, then the RAM address.
1574 15F5 E1
1575 15F6 C1
1576 15F7 79
1577 15F8 C1
1578 15F9 D1
1579 15FA E3
1580 15FB C5
1581 15FC 44
1582 15FD 4D
1583 15FE 6F
1584
1585 15FF CD 14F6
1586 1602 67
1587 1603 0A
1588 1604 BC
1589 1605 CA 1611
1590 1608 CD 14CC
1591
1592 ; BC = BAD RAM ADDRESS DE = BAD ROM ADDRESS
1593 ; H = RAM BYTE L = ROM BYTE
1594 160B 6C
1595 160C 67
1596 160D F1
1597 160E AF
1598 160F 3C
1599 1610 C9
1600
1601 1611 13
1602 1612 03
1603 1613 E3
1604 1614 2B
1605 1615 7D
1606 1616 B4
1607 1617 E3
1608 1618 C2 15FF
1609 161B CD 14CC
1610 161E E1
1611 161F C9
1612
1613 ; Stack must have on top: the type from TYPECHK, the number of bytes to check,
1614 ; the EPROM address, then the RAM address.
1615 1620 E1
1616 1621 C1
1617 1622 79
1618 1623 C1
1619 1624 D1
1620 1625 E3
1621 1626 C5
1622 1627 44
1623 1628 4D
1624 1629 6F
1625 162A CD 14F6
1626 162D 02
1627 162E 03
1628 162F 13
1629 1630 E3
1630 1631 2B
1631 1632 7D
1632 1633 B4
1633 1634 E3
1634 1635 C2 162A
1635 1638 CD 14CC
1636 163B E1
1637 163C C9
1638
1639 ;
1640 ; RDEPROM Copy DE bytes from EPROM to RAM

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1641 ; H = page of RAM L = page of EPROM
1642 ; DE = # of bytes B = type of EPROM
1643
1644 163D F1
1645 163E F1
1646
1647 163F F5
1648 1640 C5
1649 1641 D5
1650 1642 E5
1651 1643 CD 1506
1652 1646 CD 14E8
1653 1649 CD 1517
1654 164C D2 1680
1655 164E CD 1620
1656 1652 C2 16B2
1657 1655 E1
1658 1656 D1
1659 1657 C1
1660 1658 F1
1661 1659 C9
1662
1663
1664
1665
1666 ; VERIFY confirms that code is burnt
1667 ; H = page of RAM L = page of EPROM
1668 ; DE = # of bytes B = type of EPROM
1669
1669 165A E1
1670 165B F1
1671 165C F5
1672 165D C5
1673 165E D5
1674 165F E5
1675 1660 CD 1506
1676 1663 CD 14E8
1677 1666 CD 1517
1678 1669 D2 1680
1679 166E CD 15F5
1680 166F C2 16B2
1681 1672 E1
1682 1673 D1
1683 1674 C1
1684 1675 F1
1685 1676 3E
1686 1678 C9 00
1687
1688 ; Give error # in A and show contradiction in source and dest. data
1689 ; BC = SOURCE ADDRESS DE = EPROM ADDRESS
1690 ; H = SOURCE BYTE L = EPROM BYTE
1691
1691 1679 F1
1692 167A F1
1693 167B F1
1694 167C F1
1695 167D 3E 02
1696 167F C9
1697
1698
1699
1700
1701 1680 E1
1702 1681 E1
1703 1682 E1
1704 1683 E1
1705 1684 E1
1706 1685 D1
1707 1686 C1
1708 1687 F1
1709 1688 3E 04
1710 168A C9
1711
1712
1713 ; BURN burns the data in RAM to the EPROM
1714 ; H = page of RAM L = page of EPROM
1715 ; DE = # of bytes B = type of EPROM
1716
1717 168B
SERVIE:
1718 168B E1
1719 168C 7C
1720 168D FE 02
1721 168F D2 1694
1722 1692 F1
1723 1693 C9
1724 1694 F1
1725
1726 1695 F5
1727 1696 C5
1728 1697 D5
1729 1698 E5
1730 1699 CD 1506
1731
1732 169C CD 14E8
1733 169F CD 1517
1734 16A2 D2 1680
1735 16A5 CD 1597
1736 16A8 CA 16B2
1737 16AB E1
1738 16AC D1
1739 16AD C1
1740 16AE F1
1741 16AF 3E 00
1742 16B1 C9
1743
1744 ; Give error # in A and show contradiction in source and dest. data
1745 ; BC = SOURCE ADDRESS DE = EPROM ADDRESS
1746 ; H = SOURCE BYTE L = EPROM BYTE
1747
1747 16B2 F1
1748 16B3 F1
1749 16B4 F1
1750 16B5 F1
1751 16B6 3E 01
1752 16B8 C9
1753
1754
1755 ;
1756 ; ERASCHK
1757 ; Scans from address in DE, down to 0 of the EPROM type in B
1758 ; if a=0 then erased
1759 ; if a=1 then DE points to byte that isn't erased
1760
1761 16B9 E1
1762 16BA F1
1763 16BB F5
1764 16BC C5
1765 16BD D5
1766 16BE E5
1767 16BF D5
1768 16C0 48
1769 16C1 06 00
1770 16C3 C5
1771 16C4 CD 14E8
1772 16C7 CD 1517
1773 16CA D2 1682
1774 16CD CD 15DD
1775 16D0 E1
1776 16D1 C2 16DA
1777 16D4 D1
1778 16D5 C1
1779 16D6 F1
1780 16D7 3E 00
1781 16D9 C9
1782
1783 16DA C1
1784 16DB C1
1785 16DC F1
1786 16DD 3E 03
1787 16DF C9
1788
1789

```

```

1790 ;
1791 ; ZAP: BURN INITIALIZATION CODE AND MOS SERVICES FOLLOWED BY AN APPLICATION
1792 ; PROGRAM TO A 32K EPROM.
1793 ; H=upper byte of start address of application
1794 ; DE=number of bytes in application
1795 ;
1796 ; Error code is returned in A
1797 ; a=0 no error
1798 ; a=1 burn error
1799 ; a=2 verify error
1800 ; a=3 not erased (DE = ADDRESS OF BYTE NOT ERASED)
1801 ; BC=address of source data, H=source byte
1802 ; DE=address of dest data, L=dest byte
1803 ;
1804 ; no registers preserved
1805 ;
1806
1807 16E0 E1 serv20: pop h
1808 16E1 F1 pop psw
1809 16E2 F3 zap: di
1810 16E3 C5 push b
1811 16E4 E5 push h
1812 16E5 D5 push d
1813
1814 16E6 11 lxi d,07fffh ; point to top of EPROM
1815 16E9 06 mvi b,2 ; 27256 EPROM
1816 16EB CD 16BB call eraschk ; see if erased
1817 16EE B7 ora a
1818 16EF CA 16F6 jz zeras ; jmp if erased
1819 ; erase error
1820 16F2 E1 pop h ; discard old de
1821 16F3 E1 pop h
1822 16F4 C1 pop b
1823 16F5 C9 ret
1824
1825 16F6 26 00 zeras: mvi h,0 ; high byte of source addr
1826 16F8 2E 00 mvi l,0 ; high byte of dest addr
1827 16FA 11 2F00 lxi d,02f00h ; number of bytes to burn
1828 16FD 06 02 mvi b,2 ; 27256 EPROM
1829 16FF CD 1695 call burn ; burn init code and MOS services
1830 1702 B7 ora a
1831 1703 C2 1727 jnz zap_err ; if not 0, error
1832 1706 CD 165C call verify ; see if data is there
1833 1709 B7 ora a
1834 170A C2 1727 jnz zap_err ; if not 0, error
1835 170D D1 pop d ; get number of bytes
1836 170E E1 pop h ; get hi bytes of start
1837 170F E5 push h ; put them back
1838 1710 D5 push d
1839 1711 06 02 mvi b,2 ; 27256 EPROM
1840 1713 2E 2F mvi l,2fh ; burn at 2f00h
1841
1842 1715 CD 1695 call burn ; burn user's application
1843 1718 B7 ora a
1844 1719 C2 1727 jnz zap_err ; if not 0, error
1845 171C CD 165C call verify
1846 171F B7 ora a
1847 1720 C2 1727 jnz zap_err ; if not 0, error
1848 1723 D1 zap_ext: pop d
1849 1724 E1 pop h
1850 1725 C1 pop b
1851 1726 C9 ret
1852
1853 1727 zap_err:
1854 ; error occured in BURN or VERIFY, returning error data in
1855 ; de,hl,bc so remove old values from stack
1856 1727 33 INX sp
1857 1728 33 INX sp ; discard de
1858 1729 33 INX sp ; discard hl
1859 172A 33 INX sp ; discard hl
1860 172B 33 INX sp ; discard bc
1861 172C 33 INX sp ; discard bc
1862 172D C9 ret
1863
1864
1865 ;*****
1866 ; D = bits indicate what dec pnts on
1867 172E serv21:
1868 172E C5 push b
1869 172F D5 push d
1870
1871 1730 06 00 mvi b,0
1872 1732 05 dcr b
1873 1733 7A mov a,d
1874 1734 F5 push psw
1875 1735
1876 1735 04 dplup: inr b
1877 1736 78 mov a,b
1878 1737 FE 06 cpi 6
1879 1739 CA 175F jz serv21a
1880
1881 173C 3E 60 mvi a,rdrgtdsp ;comand to read dsp
1882 173E 80 add b ;COMMAND TO READ DIGIT
1883 173F D3 41 out dspcmd
1884
1885 1741 DB 40 in dspout ;GET SEGMENT VALUES
1886 1743 57 mov d,a ;SAVE A REG
1887
1888 1744 3E 80 mvi a,rgtdsp ;COMMAND TO WRITE DIGIT
1889 1746 80 add b
1890 1747 D3 41 out dspcmd
1891
1892 1749 F1 pop psw
1893 174A 1F rax
1894 174B F5 push psw
1895 174C DA 1757 jc dpon
1896 174F
1897 174F 7A dpoff: mov a,d
1898 1750 F7 ani 11110111b ;TURN OFF DECIMAL POINT
1899 1752 D3 40 out dspout ;WRITE A TO DIGIT
1900 1754 C3 1735 jmp dplup
1901 1757
1902 1757 7A dpon: mov a,d
1903 1758 F6 08 ori 00001000b
1904 175A D3 40 out dspout
1905 175C C3 1735 jmp dplup
1906 175F
1907 175F F1 serv21a: pop psw
1908 1760 D1 pop d
1909 1761 C1 pop b
1910 1762 E1 pop h
1911 1763 F1 pop psw
1912 1764 C9 ret
1913
1914 ;*****
1915 ;
1916 ;BEFORE: DE = BIN #, AFTER: DE = BCD #
1917
1918 1765 F5 bin2bcd: push psw
1919 1766 E5 push h
1920 1767
1921 1767 C5 serv22: push b
1922 1768 08 04 mvi c,4 ; max number of digits to display
1923 176A 21 0000 lxi h,0 ; init to 0
1924
1925 176D E5 bedlup: push h ; this will hold the BCD value
1926 176E EB ; hl = de
1927 176F 11 lxi d,10
1928 1772 CD 114A call div16 ; divide hex value by 10 dec
1929 1775 7B mov a,e ; e= lowest decimal digit
1930 1776 EB xchg ; save hl in de
1931 1777 E1 pop h ; get bcd value
1932 1778 CD 0093 call digit4 ; rotate left 1 digit and insert Accumulator
1933 ; in the low nibble
1934 177B 0D dcr c
1935 177C C2 176D jnz bedlup ; loop until hl has 4 BCD digits
1936 ; the number is inverted, change it
1937 177F 7D mov a,l
1938 1780 0F rrc

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1939 1781 0F          rrc
1940 1782 0F          rrc
1941 1783 0F          rrc          ; swap nibbles
1942 1784 6C          mov          l,h          ; save h in l
1943 1785 67          mov          h,a          ; put new l in h
1944 1786 7D          mov          a,l
1945 1787 0F          rrc
1946 1788 0F          rrc
1947 1789 0F          rrc
1948 178A 0F          rrc          ; swap the nibbles
1949 178B 6F          mov          l,a
1950 178C EB          xchg
1951 178D C1          pop          b
1952 178E E1          pop          h
1953 178F F1          pop          psw
1954 1790 C9          ret
1955
1956
1957
1958 1791 F5          ; PRE: BCD # IN DE POST: BIN # IN DE
1959 1792 E5          ;
1960
1961 1793 C5          bcd2bin:  push  psw
1962 1794 D5          push  h
1963 1795 7B          serv23:   push  b
1964 1796 E6          push  d          ;save D
1965 1797 4F          mov    a,e
1966 1798 4F          mov    c,a          ;store 1st nibble in C
1967 1799 7B          mov    a,e
1968 179A E6          ani    0f0h
1969 179B 0F          rrc
1970 179C 0F          rrc
1971 179D 0F          rrc
1972 179E 6F          mov    l,a          ;store 2nd nibble in l
1973 17A1 26          mvi    h,0          ;clear h
1974 17A3 CD          call   times10      ;mult 2nd nibble by 10
1975 17A6 06          mvi    b,0
1976 17A8 09          dad    b          ;add 1st nibble to 2nd
1977
1978 17A9 7A          mov    a,d
1979 17AA E6          ani    0fh          ;A = 3rd nibble
1980 17AC EB          xchg          ;save sum in DE
1981 17AD 6F          mov    l,a          ;L = 3rd nibble
1982 17AE 26          mvi    h,0
1983 17B0 0E          mvi    c,2
1984 17B2 CD          tyns100: call  times10     ;mult 3rd nibble by 100
1985 17B5 0D          dcr    c
1986 17B6 C2          jnz    tyns100
1987 17B9 19          dad    d          ;add result to sum
1988
1989 17BA D1          pop    d          ;restore original bcd num
1990 17BB 7A          mov    a,d
1991 17BC E6          ani    0f0h
1992 17BE 0F          rrc
1993 17BF 0F          rrc
1994 17C0 0F          rrc
1995 17C1 0F          rrc          ;A = 4th nibble
1996 17C2 EB          xchg          ;save sum in DE
1997 17C3 6F          mov    l,a
1998 17C4 26          mvi    h,0
1999 17C6 0E          mvi    c,3
2000 17C8 CD          tyn1000: call  times10     ;mult 4th nibble by 1000
2001 17CB 0D          dcr    c
2002 17CC C2          jnz    tyn1000
2003 17CF 19          dad    d          ;add result to sum
2004 17D0 EB          xchg          ;DE = answer
2005 17D1 C1          pop    b
2006 17D2 E1          pop    h
2007 17D3 F1          pop    psw
2008 17D4 C9          ret
2009
2010 17D5          times10:
2011 17D5 D5          push  d
2012 17D6 54          mov   d,h
2013 17D7 5D          mov   e,l
2014 17D8 19          dad   d
2015 17D9 19          dad   d
2016 17DA 19          dad   d
2017 17DB 19          dad   d
2018 17DC 19          dad   d
2019 17DD 19          dad   d
2020 17DE 19          dad   d
2021 17DF 19          dad   d
2022 17E0 19          dad   d
2023 17E1 D1          pop   d
2024 17E2 C9          ret
2025
2026
2027
2028 17E3          serv24:
2029 17E3 C5          push  b
2030 17E4 D5          push  d
2031 17E5 21          lxi   d,0
2032 17E8 AF          xra   a
2033 17E9 BA          cmp   d
2034 17EA CA          jz    readlup
2035 17ED CD          call  daddr
2036 17F0          readlup:
2037 17F0 CD          call  rdkey
2038 17F3 FE          cpi   17h
2039 17F5 CA          jz    rdexit
2040 17F8 FE          cpi   13h
2041 17FA D2          jnc   readlup
2042 17FD CD          call  digit4
2043 1800 D1          pop   d
2044 1801 D5          push  d
2045 1802 7A          mov   a,d
2046 1803 FE          cpi   0
2047 1805 CA          jz    readlup
2048 1808 CD          call  daddr
2049 180B C3          jmp   readlup
2050 180E          rdexit:
2051 180E D1          pop   d
2052 180F EB          xchg
2053 1810 C1          pop   b
2054 1811 E1          pop   h
2055 1812 F1          pop   psw
2056 1813 C9          ret
2057
2058
2059
2060
2061
2062
2063
2064 0040=          ;*****
2065 00C0=          ; EPROM PROGRAMER
2066
2067 1814          ;-----
2068 1814 3E          ;CONSTANTS
2069 1816 32          ;-----
2070 1819 CD          ;
2071
2072 181C          OFFSET EQU 40H
2073 181C CD          OFST64 EQU 0COH
2074 181F 11          ;-----
2075 1822 CD          EPRMPRO: MVI A,2 ;DEFAULT EPROM TYPE
2076 1825 3A          STA ETYP ;DEFAULT ETYP 2
2077 1828 FE          CALL DDATA
2078 182A CA          ;
2079 182D FE          ;
2080 182F CA          ;
2081 1832 FE          ;
2082 1834 CA          ;
2083 1837 FE          ;
2084 1839 CA          ;
2085 183C FE          ;
2086 183E CA          ;
2087 1841 FE          ;
2071
2072 181C CD          DSPLYMAIN: CALL CLRSCR
2073 181C CD          CALL D,MES_MAIN ;PRINT MAIN MENU
2074 181F 11          CALL PSTRNG
2075 1822 CD          LDA ETYP
2076 1825 3A          CPI 0
2077 1828 FE          JZ MESS0
2078 182A CA          CPI 1
2079 182D FE          JZ MESS1
2080 182F CA          CPI 2
2081 1832 FE          JZ MESS2
2082 1834 CA          CPI 3
2083 1837 FE          JZ MESS3
2084 1839 CA          CPI 4
2085 183C FE          JZ MESS4
2086 183E CA          CPI 5
2087 1841 FE

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2088 1843 CA 184C
2089 1846
2090 1846 21 2002 MESS6: LXI H,TXT_6TYPE
2091 1849 C3 186D JMP DSPLRST
2092 184C MESS5: LXI H,TXT_5TYPE
2093 184C 21 1FDF JMP DSPLRST
2094 184F C3 186D
2095 1852 MESS4: LXI H,TXT_4TYPE
2096 1852 C1 1FBC JMP DSPLRST
2097 1855 C3 186D
2098 1858 MESS3: LXI H,TXT_3TYPE
2099 1858 21 1F99 JMP DSPLRST
2100 185B C3 186D
2101 185E MESS2: LXI H,TXT_2TYPE
2102 185E 21 1F76 JMP DSPLRST
2103 1861 C3 186D
2104 1864 MESS1: LXI H,TXT_1TYPE
2105 1864 21 1F52 JMP DSPLRST
2106 1867 C3 186D
2107 186A MESS0: LXI H,TXT_0TYPE
2108 186A 21 1F2E DSPLRST:
2109 186D MVI D,26
2110 186D 16 1A TYPLUP:
2111 186F MOV E,M
2112 186F 5E H
2113 1870 23 INX H
2114 1871 CD 10BE CALL CONOUT
2115 1874 15 DCR D
2116 1875 C2 186F JNZ TYPLUP
2117
2118 1878 11 1EBC LXI D,MES_RSTMAN
2119 187B CD 10CD CALL PSTRNG
2120 187E
2121 187E 1E 04 MVI E,4
2122 1880 16 05 MVI D,5
2123 1882 21 2178 LXI H,LED_B
2124 1885 CD 148A CALL LEDSTR
2125
2126 1888 CD 1CB1 CALL CRLF ;CARRAGE RETURN,LINE FEED
2127 188B CD 1CB1 CALL CRLF
2128 188E CD 1CBE CALL PROMPT ;PLACE '>'
2129 1891
2130 1891 CD 33F6 NOPRMT: CALL GETCHAR ;WAIT FOR KEYPRESS FROM TERMINAL
2131 1894 FE 1B CPI 1BH ;IF ESCAPE JUMP WAIT
2132 1896 CA 187E JZ WAIT
2133
2134 1899 5F MOV E,A ;E = CHAR
2135 189A CD 10BE CALL CONOUT ;ECHO CHARACTER TO TERMINAL
2136 189D FE 3F CPI '?' ;IF ? THEN REDRAW MAIN MENU
2137 189F CA 181C JZ DSPLYMAIN
2138 18A2 B6 DF ANI 11011111B ;REMOVE CASE SENSITIVITY
2139 18A4 FE 54 CPI 'T'
2140 18A6 CA 18E3 JZ S_LCTTYPE ;IF S THEN JMP
2141 18A9 FE 52 CPI 'R'
2142 18AB CA 1960 JZ R_EAD
2143 18AE FE 41 CPI 'A'
2144 18B0 CA 197C JZ A_UTO
2145 18B3 FE 4D CPI 'M'
2146 18B5 CA 19A2 JZ M_ODMEM
2147 18B8 FE 44 CPI 'D'
2148 18BA CA 19D6 JZ D_UMP
2149 18BD FE 42 CPI 'B'
2150 18BF CA 1A17 JZ B_URN
2151 18C2 FE 56 CPI 'V'
2152 18C4 CA 1A43 JZ V_ERIFY
2153 18C7 FE 5A CPI 'Z'
2154 18C9 CA 1A6B JZ Z_AP
2155 18CB FE 45 CPI 'E'
2156 18CE CA 1AA3 JZ E_RSECHK
2157 18D1 FE 4C CPI 'L'
2158 18D3 CA 1AF6 JZ L_OADMEM
2159 18D6 FE 53 CPI 'S'
2160 18D8 CA 1B51 JZ U_PLOAD
2161 18DB FE 43 CPI 'C'
2162 18DD CA 1B3B JZ C_LRMEM
2163 18E0 C3 187E JMP WAIT ;IF NONE OF THE ABOVE, WAIT FOR NEW CHAR
2164
2165 ;-----
2166 ; DISPLAYS "ETYP" AND WAITS FOR USER TO SELECT TYPE
2167
2167 18E3 S_LCTTYPE:
2168 18E3 CD 1D6B CALL CLRSCR
2169 18E6 11 1F25 LXI D,MES_LSTYPE ;PRINT SELECT EPROM MESS
2170 18E9 CD 10CD CALL PSTRNG
2171 18EC 11 1EF9 LXI D,MES_SLCT ;PRINT 'ENTER SELECTION'
2172 18EF CD 10CD CALL PSTRNG
2173 18F2 CD 1CBE CALL PROMPT ;PLACE PROMPT
2174 18F5 CD 33F6 CALL GETCHAR ;WAIT FOR CHAR FROM TRMINL
2175 18F8 5F MOV E,A
2176 18F9 CD 10BE CALL CONOUT ;ECHO CHARACTER
2177 18FC FE 30 CPI '0'
2178 18FE CA 1922 JZ MTYPE0 ;JMP IF 0
2179 1901 FE 31 CPI '1'
2180 1903 CA 192A JZ MTYPE1 ;JMP IF 1
2181 1906 FE 32 CPI '2'
2182 1908 CA 1932 JZ MTYPE2
2183 190B FE 33 CPI '3'
2184 190D CA 193A JZ MTYPE3
2185 1910 FE 34 CPI '4'
2186 1912 CA 1942 JZ MTYPE4
2187 1915 FE 35 CPI '5'
2188 1917 CA 194A JZ MTYPE5
2189 191A FE 36 CPI '6'
2190 191C CA 1952 JZ MTYPE6
2191 191F C3 18E3 JMP S_LCTTYPE
2192
2193 1922 3E 00 MVI A,0
2194 1924 32 FFFE STA ETYP ;STORE 0 IN TYPE
2195 1927 C3 1957 JMP MOVE
2196
2197 192A 3E 01 MVI A,1
2198 192C 32 FFFE STA ETYP ;STORE 1 IN TYPE
2199 192F C3 1957 JMP MOVE
2200
2201 1932 3E 02 MVI A,2
2202 1934 32 FFFE STA ETYP
2203 1937 C3 1957 JMP MOVE
2204
2205 193A 3E 03 MVI A,3
2206 193C 32 FFFE STA ETYP
2207 193F C3 1957 JMP MOVE
2208
2209 1942 3E 04 MVI A,4
2210 1944 32 FFFE STA ETYP
2211 1947 C3 1957 JMP MOVE
2212
2213 194A 3E 05 MVI A,5
2214 194C 32 FFFE STA ETYP
2215 194F C3 1957 JMP MOVE
2216
2217 1952 3E 06 MVI A,6
2218 1954 32 FFFE STA ETYP
2219
2219 1957 3A FFFE LDA ETYP
2220 1957 3A FFFE CALL DDATA
2221 195A CD 0109 CALL DSPLYMAIN ;GOTO MAIN
2222 195D C3 181C JMP
2223
2224 ;-----
2225 ; GETS START ADD AND # BYTES AND LOADS MEM WITH EPROM DATA
2226
2227 1960
2228 1960 CD 1C8B R_EAD: CALL SIMENU ;GET ADD AND #B
2229 1963 CD 1969 CALL R_EAD ;CALL READ
2230 1966 C3 187E JMP WAIT ;GOTO MAIN
2231
2232 1969
2233 1969 78 R_EAD: MOV A,B ;ABORT IF ESC CHAR
2234 196A FE 00 CPI 0
2235 196C C2 197B JNZ EXITRD
2236 196F CD 1D43 CALL LDSTK ;PUT DATA ON STACK

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2237 1972 CD 14E8 CALL BNCNT
2238 1975 CD 1517 CALL TYPECHK
2239 ;JNC ERRTRYP
2240 1978 CD 1620 CALL READEPR
2241 197B EXITRD: RET
2242 197B C9
2243
2244
2245 ;-----
2246 A_UTO: ;CALLS ERASE CHK, BURN, THEN VIFY
2247 197C CD 1C8B CALL SIMENU
2248 197F 78 MOV A,B
2249 1980 FE 00 CPI 0 ;IF ESCAPE
2250 1982 C2 187E JNZ WAIT ; THEN ABORT
2251 1985 E5 PUSH H
2252 1986 D5 PUSH D
2253 1987 C5 PUSH B
2254 1988 CD 1AA9 CALL E_RSECHK
2255 198B C1 POP B
2256 198C D1 POP D
2257 198D E1 POP H
2258 198E FE 00 CPI 0 ;IF ESCAPE
2259 1990 C2 187E JNZ WAIT ; THEN ABORT
2260 1993 E5 PUSH H
2261 1994 D5 PUSH D
2262 1995 C5 PUSH B
2263 1996 CD 1A20 CALL B_URN
2264 1999 C1 POP B
2265 199A D1 POP D
2266 199B E1 POP H
2267 199C CD 1A4C CALL V_ERIFY
2268 199F C3 187E JMP WAIT ;GOTO MAIN MENU
2269
2270 ;-----
2271 M_ODMEM:
2272 19A2 CD 1C50 CALL S2MENU ;GET START ADD
2273 19A5 78 MOV A,B
2275 19A6 FE 00 CPI 0 ;ABORT IF ESC
2276 19A8 C2 19D3 JNZ XITMOD
2277 19AB CD 1CB1 CALL CRLF ;CARRIAGE RETURN
2278 19AE
2279 19AE CD 1C15 MODAGAN: CALL DSPLYADD ;DISPLAY ADD
2280 19B1 CD 1C37 CALL DSPLDATA ;DISPLAY DTA
2281 19B4 D5 PUSH D
2282 19B5 1E 1D MVI E,29 ;LEFT ARROW
2283 19B7 CD 10BE CALL CONOUT ;SENT TO TRMINAL
2284 19BA 1E 1D MVI E,29 ;LEFT ARROW
2285 19BC CD 10BE CALL CONOUT ;SND TO TRMINAL
2286 19BF D1 POP D
2287 19C0 CD 1CC6 CALL ASC_TRM2H ;GET NEW DATA
2288 19C3 78 MOV A,B
2289 19C4 FE 03 CPI 3 ;IF "ENTER" THEN NO MODIFY
2290 19C6 CA 19CF JZ NOMOD
2291 19C9 FE 00 CPI 0 ;IF ESC THEN EXIT
2292 19CB C2 19D3 JNZ XITMOD
2293 19CE 71 MOV M,C ;MOV NEW VAL TO MEM
2294 19CF
2295 19CF 23 INX H ;INCREMENT POINTER
2296 19D0 C3 19AE JMP MODAGAN ;LOOP
2297 19D3 C3 187E XITMOD: JMP WAIT
2298 19D3
2299
2300 ;-----
2301 19D6 CD 1C8B D_UMP: CALL SIMENU ;GET ADDRESS AND # BYTES
2302 19D9 78 MOV A,B
2304 19DA FE 00 CPI 0
2305 19DC C2 1A14 JNZ EXITDMP ;EXIT IF ESC
2306 19DF CD 1CB1 CALL CRLF
2307 19E2
2308 19E2 0E 10 NEWSCR: MVI C,16 ;16 LINES AT A TIME
2309 19E4
2310 19E4 06 10 NEWROW: MVI B,16 ;B = NUM BYTES PER LINE
2311 19E6 CD 1C15 CALL DSPLYADD
2312 19E9
2313 19E9 CD 1C37 LUPER: CALL DSPLDATA
2314 19EC 2B H ;INCREMENT POINTER
2315 19ED 1B DCX D ;DECREMENT BYTE COUNTER
2316 19EF AF XRA A ;A = 0
2317 19EF BB CMP E ;IF E NOT 0
2318 19F0 C2 19F7 JNZ DMPAGAIN ; THEN DUMP ANOTHER BYTE
2319 19F3 BA CMP D ;IF E AND D 0
2320 ; THEN EXIT
2321 19F4 CA 1A14 DMPAGAIN: JZ EXITDMP
2322 19F7 05 DCR B ;DECREMENT B
2324 19F8 C2 19E9 JNZ LUPER ;IF NOT 0 THEN DUMP AGAIN
2325 19FB 0D DCR C
2326 19FC D2 19E4 JNZ NEWROW
2327 19FF C5 PUSH D
2328 1A00 E5 PUSH H
2329 1A01 CD 1CB1 CALL CRLF ;CAIRAGE RETURN
2330 1A04 11 201F LXI D,MES_MORE
2331 1A07 CD 10CD CALL PSTRNG ;DISPLAY MORE MESS
2332 1A0A CD 33F6 CALL GETCHAR
2333 1A0D FE 1B CPI 1BH ;IF NOT ESC THEN DO AGAIN
2334 1A0F E1 POP H ; ELSE EXIT
2335 1A10 D1 POP D
2336 1A11 C2 19E2 JNZ NEWSCR
2337
2338 1A14 EXITDMP:
2339 1A14 C3 187E JMP WAIT
2340
2341 1A17 B_URN:
2342 1A17 CD 1C8B CALL SIMENU ;GET STRT ADD AND # BYTES
2343 1A1A CD 1A20 CALL B_URN
2344 1A1D C3 187E JMP WAIT ;GOTO MAIN
2345 1A20
2346 1A20 78 B_URN: MOV A,B ;IF A NON HEXVAL
2347 1A21 FE 00 CPI 0 ;THEN RET
2348 1A23 C2 1A3B JNZ EXITBRN
2349 1A26 CD 1D43 CALL LDSTK
2350 1A29 CD 14E8 CALL BNCNT
2351 1A2C CD 1517 CALL TYPECHK
2352 1A2F D2 1A42 JNC ERRTRYP
2353 1A32 CD 1597 CALL BRNIT
2354 1A35 CA 1A3C JZ ERRBRN ;IF NO ERR THEN DISPLAY NO ERR MES
2355 1A38 CD 1BF7 CALL EPMGOOD
2356 1A3B C9 EXITBRN: RET
2357 1A3B C9 ERRBRN: CALL EPMERR
2358 1A3C CD 1C06 JMP EXITBRN
2359 1A3C CD 1A3B ERRTRYP:
2360 1A3F C3 1A3B RST 7
2361 1A42
2362 1A42 FF
2363
2364 1A43 V_ERIFY:
2365 1A43 CD 1C8B CALL SIMENU ;GET ADD & #BYTES
2366 1A46 CD 1A4C CALL V_ERIFY ;CALL VERIFY
2367 1A49 C3 187E JMP WAIT ;GOTO WAIT
2368 1A4C
2369 1A4C 78 V_ERIFY: MOV A,B
2370 1A4D FE 00 CPI 0
2371 1A4F C2 1A64 JNZ EXITVRFY
2372 1A52 CD 1D43 CALL LDSTK
2373 1A55 CD 14E8 CALL BNCNT
2374 1A58 CD 1517 CALL TYPECHK
2375 1A5B CD 15F5 CALL VERIF
2376 1A5E C2 1A65 JNZ ERRVER
2377 1A61 CD 1BF7 CALL EPMGOOD
2378 1A64 EXITVRFY: RET
2379 1A64 C9 ERRVER:
2380 1A65 CD 1C06 CALL EPMERR
2381 1A65 CD 1A64 JMP EXITVRFY
2382 1A68 C3 1A64
2383
2384 1A6B Z_AP:
2385 1A6B 3A LDA ETPY

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2386 1A6E FE 02 CPI 2
2387 1A70 CA 1A7F JZ DOZAP
2388 1A73 CD 1CB1 CALL CRLF
2389 1A76 11 2156 LXI D,MES_MUSTTYP2
2390 1A79 CD 10CD CALL PSTRNG
2391 1A7C C3 187E JMP WAIT
2392 1A7F
DOZAP: CALL SIMENU
2393 1A7F CD 1C8B MOV A,B
2394 1A82 78 CPI 0
2395 1A83 FE 00 JNZ WAIT
2396 1A85 C2 187E CALL ZAP
2397 1A88 CD 16E2 CPI 3
2398 1A8B FE 03 CZ NOTERASE
2399 1A8D CC 1AE8 CZ 2
2400 1A90 FE 02 CPI 2
2401 1A92 CC 1C06 CZ EPMERR
2402 1A95 FE 00 CPI 0
2403 1A97 CC 1BF7 CZ EPMGOOD
2404 1A9A C3 187E JMP WAIT
2405 1A9D
ZAPBAD: CALL EPMERR
2406 1A9D CD 1C06 JMP WAIT
2407 1AA0 C3 187E
-----
E_RSECHK: CALL E_RSECHK
2409 1AA3
2410 1AA3 CD 1AA9 JMP WAIT
2411 1AA6 C3 187E
E_RSECHK: LDA ETYP
2412
2413 1AA9
2414 1AA9 3A FFFE CPI 5
2415 1AAC FE 05 JNC SZ2K
2416 1AAE D2 1ACD CPI 3
2417 1AB1 FE 03 JNC SZ16K
2418 1AB3 D2 1AC7 CPI 2
2419 1AB6 FE 02 CPI 2
2420 1AB8 D2 1AC1 JNC SZ32K
2421 1ABB
SZ64K: LXI D,0FFFFH
2422 1ABB 11 FFFF JMP BLNKCHK
2423 1ABE C3 1AD0
SZ32K: LXI D,07FFFFH
2424 1AC1
2425 1AC1 11 7FFF JMP BLNKCHK
2426 1AC4 C3 1AD0
SZ16K: LXI D,03FFFFH
2427 1AC7
2428 1AC7 11 3FFF JMP BLNKCHK
2429 1ACA C3 1AD0
SZ8K: LXI D,01FFFFH
2430 1ACD
2431 1ACD 11 1FFF
BLNKCHK: LDA ETYP
2432 1AD0
2433 1AD0 3A FFFE CPI 0
2434 1AD3 FE 00 JNZ SKPOS4
2435 1AD5 C2 1AD9 INR A
2436 1AD8 3C
SKPOS4: MOV B,A
2437 1AD9
2438 1AD9 47 CALL ERASCHK
2439 1ADA CD 16BB CPI 0
2440 1ADD FE 00 JNZ NOTERASE
2441 1ADF C2 1AE8
ISERASED: LXI D,MES_BLNK
2442 1AE2
2443 1AE2 11 2089 JMP ERSMESS
2444 1AE5 C3 1AEB
NOTERASE: LXI D,MES_NBLNK
2445 1AE8
2446 1AE8 11 206F
ERSMESS: PUSH PSW
2447 1AEB
2448 1AEB P5 PUSH H
2449 1AEC E5 CALL CRLF
2450 1AED CD 1CB1 CALL PSTRNG
2451 1AF0 CD 10CD CALL H
2452 1AF3 E1 POP PSW
2453 1AF4 F1 POP PSW
2454 1AF5 C9 RET
2455
2456
-----
L_LOADMEM: CALL CRLF
2457 1AF6 CD 1CB1 LXI D,MES_RDYUPLD
2458 1AF6 CD 11 211C CALL PSTRNG
2459 1AF6 CD 10CD CALL CRLF
2460 1AFC CD 10CD MVI E,0
2461 1AFF CD 1CB1
2462 1B02 1E 00
;
2463
2464 1B04 3A FFFE LDA ETYP
2465 1B07 FE 00 CPI 0
2466 1B09 16 C0 MVI D,0FST64
2467 1B0B CA 1B10 JZ SKPOS2
2468 1B0E 16 40 MVI D,OFFSET
2469 1B10
SKPOS2: CALL HEX1CON
2470 1B10 CD 332D LXI D,MES_UPLDCMPLT
2471 1B13 11 209F RAR
2472 1B16 1F RAR
2473 1B17 1F RAR
2474 1B18 D2 1B21 JNC CHKNOHEX
2475 1B1B
CHKSUMER: LXI D,MES_CHKSUM
2476 1B1B 11 2104 JMP XITL_MEM
2477 1B1E C3 1B32
CHKNOHEX: RAR
2478 1B21
2479 1B21 1F RAR
2480 1B22 D2 1B2B JNC ESCERR
2481 1B25 11 20B8 LXI D,MES_NONHEX
2482 1B28 C3 1B32 JMP XITL_MEM
2483 1B2B
ESCERR: RAR
2484 1B2B 1F RAR
2485 1B2C D2 1B32 JNC XITL_MEM
2486 1B2F 11 20DF LXI D,MES_ESCERR
2487 1B32
XITL_MEM: CALL CRLF
2488 1B32 CD 1CB1 CALL PSTRNG
2489 1B35 CD 10CD JMP WAIT
2490 1B38 C3 187E
;*****
; WRITES FF TO ALL MEM LOCATIONS FROM 4000 TO BFFF
-----
C_LRMEM: LXI H,4000H
2494 1B3B
2495 1B3B 21 4000 MVI B,OFFH
2496 1B3E 06 FF CLRAGN:
2497 1B40
2498 1B40 70 MOV M,B
2499 1B41 23 INX
2500 1B42 7D MOV A,L
2501 1B43 FE 00 CPI 0
2502 1B45 C2 1B40 JNZ CLRAGN
2503 1B48 7C MOV A,H
2504 1B49 FE C0 CPI 0C0H ;CLEAR TO C000
2505 1B4B C2 1B40 JNZ CLRAGN
2506 1B4E C3 187E JMP WAIT
-----
U_PLOAD: CALL SIMENU
2508
2509 1B51
2510 1B51 CD 1C8B MOV A,B
2511 1B54 78 CPI 0
2512 1B55 FE 00 JNZ WAIT ;EXIT IF ESC
2513 1B57 C2 187E PUSH D
2514 1B5A D5 E5 PUSH H
2515 1B5B E5
2516 1B5C CD 1CB1 CALL CRLF
2517 1B5F 11 2136 LXI D,MES_PRSENTR
2518 1B62 FE 10CD CALL PSTRNG
2519 1B65 CD 33F6 CALL GETCHAR
2520 1B68 FE 1B CPI 1BH
2521 1B6A E1 POP H
2522 1B6B D1 POP D
2523 1B6C CA 187E JZ WAIT
2524 1B6F CD 1B75 CALL INTELUP
2525 1B72 C3 1891 JMP NOPRMT
;*****
;
; THIS ROUTINE TAKES DATA THAT STARTS AT HL AND # BYTES AT DE AND SENDS IT TO
; THE OUTPUT PORT AS AN INTEL HEXFILE.
;
-----
INTELUP: PUSH H
2532 1B75
2533 1B75 E5 H ;SAVE H WHICH POINTS TO SYS RAM
2534 1B76 13 INX D

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2535 1B77 3A      FFFE          LDA          ETYPE
2536 1B7A FE      00          CPI          0
2537 1B7C 06      40          MVI          B,OFFSET
2538 1B7E C2      1B83       JNZ          NOTOS
2539 1B81 06      C0          MVI          B,OFST64
2540 1B83
2541 1B83 7C      NOTOS:      MOV          A,H
2542 1B84 90      SUB          B
2543 1B85 67      MOV          H,A      ;NEW H HAS OFFSET SUBTRACTED
2544
2545 1B86 06      10          HXLUP:      MVI          B,10H      ;DATA BYTES PER LINE OF HEX
2546
2547 1B88 7A      MOV          A,D      ;MOVE D TO A TO CHECK FOR 0
2548 1B89 FE      00          CPI          0      ;SEE IF 0
2549 1B8B C2      1B9A       JNZ          DOHEXL      ;IF NOT 0 THEN DE > 10H
2550
2551 1B8E 7B      MOV          A,E
2552 1B8F FE      11          CPI          11H      ;SEE IF A FULL LINE OF HEX
2553 1B91 D2      1B9A       JNC          DOHEXL      ;OUTPUT A HEX LINE
2554
2555          ;THE DATA BYTES ARE NOW LESS THAN 10H
2556 1B94 43      MOV          B,E      ;BYTES = E. THIS IS THE LAST DATA FIELD
2557 1B95 AF      XRA          A      ;CLEAR A
2558 1B96 B8      CMP          B
2559 1B97 CA      1BA0       JZ           FINHEX      ;IF B=0 THEN EXIT
2560
2561 1B9A CD      DOHEXL:    CALL         HEXLINE      ;OUTPUT A LINE OF HEX
2562 1B9D C3      1B86       JMP          HXLUP      ;DO NEXT LINE OF HEX
2563
2564 1BA0          FINHEX:
2565 1BA0          LASTHX:
2566 1BA0          LXI          H,0      ;ADDRESS = 0000
2567 1BA3 06      MVI          B,0      ;NO MORE DATA, SO B=0
2568 1BA5 CD      1BAA       CALL         HEXLINE      ;DO THE LAST LINE OF HEX
2569
2570 1BA8 E1      POP          H
2571 1BA9 C9      RET
2572
2573          ;***** END OF MAIN LOOP *****
2574
2575 1BAA 3E      3A          ;***** HEXLINE STARTS AT HL AND SENDS OUT THE # OF BYTES (HELD IN B)*****
2576 1BAC CD      1BE5       HEXLINE:    MVI          A,':'
2577 1BAF AF      CALL         PUTCHAR      ;OUTPUT THE COLON
2578          XRA          A      ;CLEAR A. A HOLDS THE RUNNING SUM
2579
2580          ;OUTPUT # OF BYTES
2581 1BB1 81      MOV          C,B
2582 1BB2 CD      1BEC       ADD          C      ;ADD BYTE TO ACCUM
2583          CALL         BIN2ASC      ;OUTPUT # OF BYTES AS ASCII
2584
2585          ;OUTPUT THE ADDRESS
2586 1BB5 4C      MOV          C,H
2587 1BB6 81      ADD          C      ;ADD TO ACCUM
2588 1BB7 CD      1BEC       CALL         BIN2ASC      ;OUTPUT ADDRESS HI AS ASCII
2589
2589 1BBA 4D      MOV          C,L
2590 1BBB 81      ADD          C      ;ADD TO ACCUM
2591 1BBC CD      1BEC       CALL         BIN2ASC      ;OUTPUT ADDRESS LO AS ASCII
2592
2593          ;OUTPUT TYPE
2594 1BBF 0E      00          MVI          C,0      ;TYPE 0
2595 1BC1 81      ADD          C      ;ADD TO ACCUM
2596 1BC2 CD      1BEC       CALL         BIN2ASC      ;OUTPUT TYPE AS ASCII
2597
2598          ;OUTPUT DATA (IF THERE IS SOME)
2599 1BC5 04      INR          B      ;INC IN CASE OF 1 BYTE, (B=1)
2600 1BC6 05      DCR          B      ;DECREMENT BYTE COUNTER
2601 1BC7 CA      1BDB       JZ           CHCKSUM      ;JUMP IF B=0
2602          ;**
2603 1BCA 33      INX          SP
2604 1BCB 33      INX          SP
2605 1BCC E3      XTHL
2606 1BCD 4E      MOV          C,M      ;LOAD A BYTE FROM THE DATA STRING
2607 1BCE 81      ADD          C      ;ADD TO ACCUM
2608 1BCF 23      INX          H      ;POINT TO NEXT BYTE IN MEMORY
2609 1BD0 E3      XTHL
2610 1BD1 3B      DCX          SP
2611 1BD2 3B      DCX          SP
2612          ;**
2613 1BD3 CD      1BEC       CALL         BIN2ASC      ;OUTPUT DATA AS ASCII
2614
2615 1BD6 23      INX          H      ;POINT TO THE NEXT BYTE
2616 1BD7 1B      DCX          D      ;1 LESS BYTE TO OUTPUT
2617 1BD8 C3      1BC6       JMP          LINLUP
2618
2619          ;OUTPUT CHECKSUM
2620 1BDB 2F      CHCKSUM:   CMA
2621 1BDC 3C      INR          A      ;CHECKSUM = 2'S COMPLEMENT OF SUM
2622 1BDD 4F      MOV          C,A
2623 1BDE CD      1BEC       CALL         BIN2ASC      ;OUTPUT THE CHECKSUM
2624 1BE1 CD      1CB1       CALL         CRLF
2625
2626 1BE4 C9      RET
2627
2628          ;***** END OF HEXLINE *****
2629          ;PATCH FOR INTELUP
2630 1BE5          PUTCHAR:
2631 1BE5 D5      PUSH         D
2632 1BE6 5F      MOV          E,A
2633 1BE7 CD      10BE       CALL         CONOUT
2634 1BE8 D1      POP          D
2635 1BE9 C9      RET
2636
2637          ;*****
2638          ;PATCH FOR INTELUP
2639 1BEC          BIN2ASC:
2640 1BEC P5      PUSH         PSW
2641 1BED D5      PUSH         D
2642 1BEE C5      PUSH         B
2643 1BEF 79      MOV          A,C
2644 1BF0 CD      11E4       CALL         HEX2
2645 1BF1 F3      POP          B
2646 1BF2 D1      POP          D
2647 1BF3 F1      POP          PSW
2648 1BF4 C9      RET
2649
2650          ;*****
2651 1BF7          EPMGOOD:
2652 1BF7 E5      PUSH         H
2653 1BF8 CD      1CB1       CALL         CRLF
2654 1BF9 CD      1CB1       CALL         CRLF
2655 1BFA 11      LXI          D,MES_EGOOD
2656 1BFB ED      10CD       CALL         PSTRNG
2657 1BFC E1      POP          H
2658 1C05 C9      RET
2659
2660          ;*****
2661          ;DISPLAYS EPROM ERROR MESSAGE
2662          ;*****
2663 1C06          EPMERR:
2664 1C06 E5      PUSH         H
2665 1C07 CD      1CB1       CALL         CRLF
2666 1C08 CD      1CB1       CALL         CRLF
2667 1C09 11      LXI          D,MES_EERR
2668 1C0A ED      10CD       CALL         PSTRNG
2669 1C0B CD      10CD       CALL         CD
2670 1C0C E1      POP          H
2671 1C0D C9      RET
2672
2673          ;*****
2674          ; PRE: ADDRESS IN HL
2675          ; POST: NONE
2676          ;*****
2677 1C15          ;DISPLAYS TO CONSOLE THE ADDRESS IN HL - OFFSET, PLACES ':' AFTER
2678 1C15          DSPLYADD:
2679 1C15 D5      PUSH         D      ;SAVE D
2680 1C16 CD      1CB1       CALL         CRLF
2681 1C17 3A      LDA          FFFE
2682 1C18 00      CPI          0
2683 1C19 7C      MVI          A,H
2684 1C1A 7C      MOV          A,H      ;DISPLAY H
2685 1C1B CA      1C27       JZ           SKPOS1
2686 1C1C 40      SUI          OFFSET

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2684 1C24 C3 1C29 SKPOS1: JMP SKOS64
2685 1C27 SUI OFST64
2686 1C27 D6 C0 SKOS64:
2687 1C29 CALL HEX2
2688 1C29 CD 11E4 CALL HEX2 ;DISPLAY L
2689 1C2C 7D MOV A,L
2690 1C2D CD 11E4 CALL HEX2
2691 1C30 1E 3A MVI E,' ' ;PLACE COLLEN
2692 1C32 CD 10BE CALL CONOUT ;RESTORE D
2693 1C35 D1 POP D
2694 1C36 C9 RET
2695 ;*****
2696 ; PRE: HL = ADDRESS OF DATA
2697 ; POST: NONE
2698 ;
2699 ; PLACES ' ' THEN DATA AT HL
2700 ;-----
2701 1C37 DSPLDATA:
2702 1C37 D5 PUSH D
2703 1C38 1E 20 MVI E,' '
2704 1C3A CD 10BE CALL CONOUT
2705 1C3D 7E MOV A,M
2706 1C3E CD 11E4 CALL HEX2
2707 1C41 D1 POP D
2708 1C42 C9 RET
2709 ;*****
2710 ; PRE: HL = START ADD OF SRCCE
2711 ; DE = START ADD OF DEST
2712 ; B = # OF BYTES
2713 ; POST: NONE
2714 ;-----
2715 1C43 MEMMOVE:
2716 1C43 F5 PUSH PSW
2717 1C44 C5 PUSH B
2718 1C45 MOVAGAIN:
2719 1C45 7E MOV A,M ;MOV DATA AT ADDRESS HL TO ACC
2720 1C46 12 STAX D ;SAVE DATA IN ACC AT ADDRESS DE
2721 1C47 13 INX D ;INC DESTINATION ADDRESS
2722 1C48 23 INX H ;INC SRCCE ADDRESS
2723 1C49 05 DCR B ;DEC BYTE COUNTER
2724 1C4A C2 1C45 JNZ MOVAGAIN ;IF 0
2725 1C4D C1 POP B ; THEN EXIT
2726 1C4E F1 POP PSW
2727 1C4F C9 RET
2728 ;*****
2729 ; PRE: NONE
2730 ; POST: ADDRESS IN HL
2731 ;
2732 ; DISPLAYS "STARTING ADDRESS" ,GETS 4 HEX VALUES FROM TERMINAL IN HL
2733 ;-----
2734 1C50 S2MENU:
2735 1C50 D5 PUSH D
2736 1C51 CD 1CB1 CALL CRLF
2737 1C54 D1 1CB1 CALL CRLF
2738 1C57 11 LXI D,MES_STRTAD
2739 1C5A CD 10CD CALL PSTRNG
2740 1C5D CD 1CBE CALL PROMPT
2741 1C60 CD 1CC6 CALL ASC_TRM2H
2742 1C63 78 MOV A,B
2743 1C64 FE 00 CPI 0
2744 1C66 C2 1C89 JNZ EXITM2
2745 1C69 61 MOV H,C
2746 1C6A CD 1CC6 CALL ASC_TRM2H
2747 1C6D 78 MOV A,B
2748 1C6E FE 00 CPI 0
2749 1C70 C2 1C89 JNZ EXITM2
2750 1C73 69 MOV L,C
2751 1C74 F5 PUSH PSW
2752 1C75 3A FFFE LDA E,TYPE
2753 1C78 FE 00 CPI 0
2754 1C7A CA 1C84 JZ SK64EPM
2755 1C7D 7C MOV A,H
2756 1C7E C6 40 ADI OFFSET ;ADD OFFSET
2757 1C80 67 MOV H,A
2758 1C81 C3 1C88 JMP SKPOS
2759 1C84 SK64EPM:
2760 1C84 7C MOV A,H
2761 1C85 C6 C0 ADI OFST64
2762 1C87 67 MOV H,A
2763 1C88 SKPOS:
2764 1C88 F1 POP PSW
2765 1C89 EXITM2:
2766 1C89 D1 POP D
2767 1C8A C9 RET
2768 ;*****
2769 ; PRE: NONE
2770 ; POST: HL = ADDRESS
2771 ; DE = # OF BYTES
2772 ;-----
2773 1C8B S1MENU:
2774 1C8B CD 1C50 CALL S2MENU
2775 1C8E C2 1CB0 JNZ EXITM1
2776 1C91 E5 PUSH H
2777 1C92 11 1F12 LXI D,MES_BYTES
2778 1C95 CD 10CD CALL PSTRNG
2779 1C98 E1 POP H
2780 1C99 CD 1CBE CALL PROMPT
2781 1C9B CD 1CC6 CALL ASC_TRM2H
2782 1C9F 78 MOV A,B
2783 1CA0 FE 00 CPI 0
2784 1CA2 C2 1CB0 JNZ EXITM1
2785 1CA5 51 MOV D,C
2786 1CA6 CD 1CC6 CALL ASC_TRM2H
2787 1CA9 78 MOV A,B
2788 1CAA FE 00 CPI 0
2789 1CAC C2 1CB0 JNZ EXITM1
2790 1CAF 59 MOV E,C
2791 1CB0 EXITM1:
2792 1CB0 C9 RET
2793 ;*****
2794 ;
2795 ;-----
2796 1CB1 CRLF:
2797 1CB1 D5 PUSH D
2798 1CB2 1E 0D MVI E,0DH
2799 1CB4 CD 10BE CALL CONOUT
2800 1CB7 1E 0A MVI E,0AH
2801 1CB9 CD 10BE CALL CONOUT
2802 1CBC D1 POP D
2803 1CBD C9 RET
2804 ;*****
2805 ;
2806 ;-----
2807 1CBE PROMPT:
2808 1CBE D5 PUSH D
2809 1CBF 1E 3E MVI E,'>'
2810 1CC1 CD 10BE CALL CONOUT
2811 1CC4 D1 POP D
2812 1CC5 C9 RET
2813 ;*****
2814 ; PRE: NONE
2815 ; POST: C = 2 HEX VALUES FROM TERMINAL
2816 ; B = 1 IF ESCAPE CHAR, 2 IF NON HEX, 3 IF RETURN, 0 OTHERWISE
2817 ;-----
2818 ASC_TRM2H:
2819 1CC6
2820 1CC6 F5 PUSH PSW
2821 1CC7 D5 PUSH D
2822 1CC8 CD 33F6 CALL GETCHAR
2823 1CCB FE 1B CPI 1BH
2824 1CCD CA 1CF7 JZ ERRATH
2825 1CD0 FE 0D CPI 0DH
2826 1CD2 CA 1CF2 JZ RETATH
2827 1CD5 5F MOV E,A
2828 1CD6 47 MOV B,A
2829 1CD7 CD 10BE CALL CONOUT
2830 1CDA CD 33F6 CALL GETCHAR
2831 1CDD FE 1B CPI 1BH
2832 1CDF CA 1CF7 JZ ERRATH

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2833 1CE2 FE 0D CPI 0DH
2834 1CE4 CA 1CF2 JZ RETATH
2835 1CE7 5F MOV E,A
2836 1CE8 4F MOV C,A
2837 1CE9 CD 10BE CALL CONOUT
2838 1CEC CD 1CFC CALL ASCII2HEX
2839 1CEF C3 JMP XITATH
2840 1CF2 RETATH: MVI B,3
2841 1CF2 C6 03 JMP XITATH
2842 1CF4 C3 1CF9
2843 1CF7 ERRATH: MVI B,1
2844 1CF7 06 01 XITATH: POP D
2845 1CF9 PSW
2846 1CF9 D1 POP D
2847 1CFA F1 POP PSW
2848 1CFB C9 RET
2849
2850
2851 ;*****
2852 ;PRE: BC CONTAIN HIGH AND LO ASCII VALUES
2853 ;POST: C CONTAINS HEX VALUE. B = 1 IF ESCAPE, 2 IF NON HEX, 0 OTHERWISE
2854
2854 1CFC ASCII2HEX: PUSH PSW
2855 1CFC F5 PUSH D
2856 1CFD D5 D ;D = COUNTER
2857 1CFE 16 02 MVI D,2 ;E = INITIAL RESULT
2858 1D00 1E 00 MVI E,0 ;B = IS FIRST NUMBER
2859 1D02 78 MOV A,B
2860 1D03
2861 1D03 FE 1B CPI 1BH
2862 1D05 CA 1D38 JZ ESCPE
2863 1D08 FE 30 CPI '0'
2864 1D0A DA 1D33 JC ERROR
2865 1D0D FE 3A CPI ':'
2866 1D0F D2 1D17 JNC LETTER
2867 1D12 E6 0F ANI 0FH
2868 1D14 C3 1D25 JMP SKIP1
2869 1D17 LETTER: MVI B,1
2870 1D17 E6 DF ANI 0DFH ;MAKE UPPER CASE
2871 1D19 FE 41 CPI 'A'
2872 1D1B DA 1D33 JC ERROR
2873 1D1E FE 47 CPI 'G'
2874 1D20 D2 1D33 JNC ERROR
2875 1D23 D6 37 SUI 37H
2876 1D25 SKIP1: ORA E
2877 1D25 B3 DCR D
2878 1D26 15 DCR D
2879 1D27 CA 1D3D JZ DONE
2880 1D2A 07 RLC
2881 1D2B 07 RLC
2882 1D2C 07 RLC
2883 1D2D 07 RLC
2884 1D2E 5F MOV E,A
2885 1D2F 79 MOV A,C
2886 1D30 C3 1D03 JMP NXTCHR
2887 1D33 ERROR: MVI B,2
2888 1D33 06 02 JMP ERRDNE
2889 1D35 C3 1D40
2890 1D38 ESCPE: MVI B,1
2891 1D38 06 01 JMP ERRDNE
2892 1D3A C3 1D40
2893 1D3D DONE: MVI B,0
2894 1D3D 06 00 MOV C,A
2895 1D3F 4F ERRDNE: POP D
2896 1D40 PSW
2897 1D40 D1 POP D
2898 1D41 F1 POP PSW
2899 1D42 C9 RET
2900
2901 ; CALL HEX2 = 11DC
2902 ; PRE: HEX VALUE TO DISPLAY IN A
2903 ; POST: NONE
2904
2905 ;*****
2906 ; PRE: ADDRES OF EPROM IN HL
2907 ; # BYTES IN DE
2908 ; TYPE OF EPROM IN A
2909
2910 ;
2911 ; POST: STACK IS LOADED: TYPE TOS
2912 ; # BYTES
2913 ; EPROM ADDRESS
2914 ; RAM ADDRESS =EPROM ADDRESS + C000H
2915
2916 1D43 LDSTK: MOV B,H
2917 1D43 44 MOV C,L
2918 1D44 4D POP H
2919 1D45 81 POP B
2920 1D46 C5 PUSH B
2921 1D47 3A FFFE LDA ETYP
2922 1D4A FE 00 CPI 0
2923 1D4C CA 1D56 JZ ADOS64
2924 1D4F 78 MOV A,B
2925 1D50 D6 40 SUI OFFSET
2926 1D52 47 MOV B,A
2927 1D53 C3 1D5A JMP OSDUN
2928 1D56 ADOS64: MOV A,B
2929 1D56 78 MOV A,B
2930 1D57 D6 C0 SUI OFST64
2931 1D59 47 MOV B,A
2932 1D5A OSDUN: LDA ETYP
2933 1D5A 3A FFFE LDA ETYP
2934 1D5D FE 00 CPI 0
2935 1D5F C2 1D63 JNZ SKPTF
2936 1D62 3C SKPTF: INR A
2937 1D63
2938 1D63 C5 PUSH B
2939 1D64 D5 PUSH D
2940 1D65 4F MOV C,A
2941 1D66 06 MVI B,0
2942 1D68 C5 PUSH B
2943 1D69 E5 PUSH H
2944 1D6A C9 RET
2945
2946 ;*****
2947 ; PRE: NONE
2948 ; POST: NONE
2949
2950 1D6B CLRSCR: PUSH D
2951 1D6B D5 MVI E,0DH
2952 1D6C 1E 0D CALL CONOUT
2953 1D6E CD 10BE MVI D,24
2954 1D71 16 18 CLRcnt: MVI D,24
2955 1D73
2956 1D73 1E 0A MVI E,0AH
2957 1D75 CD 10BE CALL CONOUT
2958 1D78 15 DCR D
2959 1D79 C2 1D73 JNZ CLRcnt
2960 1D7C D1 POP D
2961 1D7D C9 RET
2962
2963 ;*****
2964
2965 1D7E MES_MAIN: DB ' PRIMER EPROM PROGRAMMER'
2966 1D7E 20 20 20 20 20 DB '
2967 1D9C 0D DB 0DH
2968 1D9D 0A DB 0AH
2969 1D9E 20 20 20 20 20 DB ' EMAC INC.'
2970 1DB6 0D DB 0DH
2971 1DB7 0A DB 0AH
2972 1DB8 0D DB 0DH
2973 1DB9 0A DB 0AH
2974 1DBA 0D DB 0DH
2975 1DBB 0A DB 0AH
2976
2977 1DBC 20 20 20 20 20 DB ' A Auto (erase chk, burn, verify)'
2978 1DE4 0D DB 0DH
2979 1DE5 0A DB 0AH
2980
2981 1DE6 20 20 20 20 20 DB ' B Burn EPROM'

```

```

2982 LDFA OD DB ODH
2983 LDFB OA DB OAH
2984
2985 LDFC 20 20 20 20 20 DB ' C Clear memory'
2986 LE12 OD DB ODH
2987 LE13 OA DB OAH
2988
2989 LE14 20 20 20 20 20 DB ' D Dump memory'
2990 LE29 OD DB ODH
2991 LE2A OA DB OAH
2992
2993 LE2B 20 20 20 20 20 DB ' E Erase check'
2994 LE40 OD DB ODH
2995 LE41 OA DB OAH
2996
2997 LE42 20 20 20 20 20 DB ' L Load memory'
2998 LE57 OD DB ODH
2999 LE58 OA DB OAH
3000
3001 LE59 20 20 20 20 20 DB ' M Modify memory'
3002 LE70 OD DB ODH
3003 LE71 OA DB OAH
3004
3005 LE72 20 20 20 20 20 DB ' R Read EPROM'
3006 LE86 OD DB ODH
3007 LE87 OA DB OAH
3008
3009 LE88 20 20 20 20 20 DB ' S Send file'
3010 LE9B OD DB ODH
3011 LE9C OA DB OAH
3012
3013 LE9D 20 20 20 20 20 DB ' T Type select < TYPE '
3014 LEBB 24 DB '$'
3015
3016 LEBE 20 MES_RSTMAN: DB ODH
3017 LEBE OD DB OAH
3018 LEBD OA DB
3019
3020 LEBE 20 20 20 20 20 DB ' V Verify EPROM'
3021 LED4 OD DB ODH
3022 LED5 OA DB OAH
3023
3024 LED6 20 20 20 20 20 DB ' Z Zap EPROM'
3025 LEE9 OD DB ODH
3026 LEEA OA DB OAH
3027
3028 LEEB 20 20 20 20 20 DB ' ? Help'
3029
3030
3031 LEF9 20 MES_SLCT: DB ODH
3032 LEF9 OD DB OAH
3033 LEFA OA DB OAH
3034 LEFB OA DB OAH
3035 LEFC OA DB OAH
3036 LEFD OA DB OAH
3037 LEFE 24 DB '$'
3038
3039 LEFF 20 MES_STRTAD: DB
3040 LEFF 20 73 74 61 72 74 DB ' starting address '
3041 LF11 24 DB '$'
3042 LF12 20 MES_BYTES: DB
3043 LF12 20 20 6E 75 6D 62 DB ' number of bytes '
3044 LF24 24 DB '$'
3045
3046
3047 LF25 20 MES_LSTYPE: DB
3048 LF25 OD DB ODH
3049 LF26 OA DB OAH
3050 LF27 20 20 20 20 20 DB '
3051 LF2E 20 20 28 36 34 4B DB '
3052 LF2E 30 DB '0 (64K X 8 HI, Vpp = 12.5)>'
3053 LF49 OD DB ODH
3054 LF4A OA DB OAH
3055 LF4B 20 20 20 20 20 DB '
3056 LF52 20 20 28 36 34 4B DB '
3057 LF52 31 DB '1 (64K X 8 LO, Vpp = 12.5)>'
3058 LF6D OD DB ODH
3059 LF6E OA DB OAH
3060 LF6F 20 20 20 20 20 DB '
3061 LF76 20 20 28 33 32 4B DB '
3062 LF76 32 DB '2 (32K X 8, Vpp = 12.5)>'
3063 LF90 OD DB ODH
3064 LF91 OA DB OAH
3065 LF92 20 20 20 20 20 DB '
3066 LF99 20 20 28 31 36 4B DB '
3067 LF99 33 DB '3 (16K X 8, Vpp = 12.5)>'
3068 LFB3 OD DB ODH
3069 LFB4 OA DB OAH
3070 LFB5 20 20 20 20 20 DB '
3071 LFB5 20 20 28 31 36 4B DB '
3072 LFB5 34 DB '4 (16K X 8, Vpp = 21.0)>'
3073 LFD6 OD DB ODH
3074 LFD7 OA DB OAH
3075 LFD8 20 20 20 20 20 DB '
3076 LFD8 20 20 28 20 38 4B DB '
3077 LFD8 35 DB '5 ( 8K X 8, Vpp = 12.5)>'
3078 LFF9 OD DB ODH
3079 LFFA OA DB OAH
3080 LFFB 20 20 20 20 20 DB '
3081 LFFB 20 20 28 20 38 4B DB '
3082 LFFB 36 DB '6 ( 8K X 8, Vpp = 21.0)>'
3083 L01C OD DB ODH
3084 L01D OA DB OAH
3085 L01E 24 DB '$'
3086
3087 L01F 20 MES_MORE: DB
3088 L01F 20 45 53 43 20 74 DB ' ESC to exit, RET to continue. '
3089 L03E 24 DB '$'
3090
3091 L03F 20 MES_EGOOD: DB
3092 L03F 20 20 20 20 20 DB ' NO ERRORS DETECTED '
3093 L059 24 DB '$'
3094 L05A 20 MES_EERR: DB
3095 L05A 20 20 20 20 20 DB ' EPROM ERROR. '
3096 L06E 24 DB '$'
3097
3098
3099 L06F 20 MES_NBLNK: DB
3100 L06F 20 20 20 20 20 DB ' DEVICE NOT ERASED '
3101 L088 24 DB '$'
3102 L089 20 MES_BLNK: DB
3103 L089 20 20 20 20 20 DB ' DEVICE ERASED '
3104 L09E 24 DB '$'
3105 L09F 20 MES_UPLDICMPLT: DB
3106 L09F 20 20 20 20 20 DB ' UPLOAD COMPLETE. '
3107 L0B7 24 DB '$'
3108 L0B8 20 MES_NONHEX: DB
3109 L0B8 20 20 20 20 20 DB ' NON HEX CHARACTER INCOUNTERED. '
3110 L0DE 24 DB '$'
3111 L0DF 20 MES_ESCERR: DB
3112 L0DF 20 20 20 20 20 DB ' ESCAPE CHARACTER ENOUNTERED. '
3113 L103 24 DB '$'
3114 L104 20 MES_CHKSUM: DB
3115 L104 20 20 20 20 20 DB ' CHECKSUM ERROR. '
3116 L11B 24 DB '$'
3117 L11C 20 MES_RDYUPLD: DB
3118 L11C 20 20 20 20 20 DB ' READY FOR UPLOAD. '
3119 L135 24 DB '$'
3120
3121 L136 20 MES_PSENTNR: DB
3122 L136 20 20 20 20 20 DB ' PRESS A KEY WHEN READY. '
3123 L155 24 DB '$'
3124
3125 L156 20 MES_MUSTTYP2: DB
3126 L156 20 20 20 20 20 DB ' MUST SELECT TYPE 2 EPROM. '
3127 L177 24 DB '$'
3128
3129 L178 C7 LED_B: DB OC7H ;"B"
3130 L179 C1 DB OClH ;"U"

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3131 217A 05          DB      005H  ;"R"
3132 217B 45          DB      045H  ;"N"
3133
3134
3135
3136
3137
3138
3139
3140
3141
3142
3143
3144 2F01 C3      3260      moscode: jmp      init_mon
3145
3146
3147
3148
3149
3150
3151
3152
3153 3FFE=          chksum      equ      3ffeh  ; address for checksum
3154 2F04 55      41 52 54 20 74      commsg: defb    'UART test'
3155 2F0D 0A      0D 3E 24      prmpt:  defb    10,13,'>$'
3156
3157 2F11          SLFTST:
3158
3159 2F11 06      06          ; set decimal pts on displays 1 to 4
3160 2F13 78          mvi      b,6      ; start at left display
3161 2F14 F6          mov      a,b      ; b= 80h-85h
3162 2F16 3D          ori      80h
3163 2F17 D3          dcr      a      ; make 1 less
3164 2F19 3E          out     dspcmd   ; select display
3165 2F1B D3          mvi      a,8     ; make a period
3166 2F1D 05          out     dspout   ; output bit pattern to the display
3167 2F1E C2          dcr      b
3168          jnz     slft1
3169
3170          ;
3171          ; Checksum the EPROM
3172          ;
3172 2F21 2A      3FFE      lhld    chksum   ; read check sum from last 2 bytes
3173 2F24 CD      30B6      call   checksum ; do a check sum from lffe down to 0
3174 2F27 7D          mov      a,l
3175 2F28 B4          ora      h
3176 2F29 CA      2F37      jz      etst2   ; if chksum=0 then skip error msg
3177 2F2C 3E          mvi      a,11
3178 2F2E CD      00CC      call   regprnt ; display bE for bad EPROM
3179 2F31 CD      016C      call   beep
3180 2F34 CD      0133      call   rdkey   ; wait here so displays aren't trashed
3181 2F37
3182          etst2:
3183          ;
3184          ; Check the RAM
3185          ;
3186
3187 2F37 3E      08          mvi      a,8
3188 2F39 CD      00CC      call   regprnt ; print "rd" on the right displays
3189
3190          ; This checks out the 32k RAM which may be in slot 1
3191          ; 1st check for pos. "A" mem map. If no ram at 8000 or 4000
3192          ; then check 8155 RAM at FF00
3193 2F3C 21      4000      lxi      h,4000h ; bc is the number of bytes to check
3194 2F3F 01      8000      lxi      b,8000h ; bc is the number of bytes to check
3195 2F42 7E          mov      a,m
3196 2F43 5C          mov      d,a     ; preserve original A
3197 2F44 37          inr      a      ; inc A
3198 2F45 34          inr      m      ; inc mem
3199 2F46 BE          cmp      m      ; They should be same if RAM
3200 2F47 72          mov      m,d     ; restore original data, in case of RAM
3201 2F48 CA      2F5B      jz      rmchk1   ; jmp if RAM at this address
3202 2F4B 7C          mov      a,h
3203 2F4C FE          cpi      80h
3204 2F4E CA      2F56      jz      rmchk3   ; if 8000 has been checked, check ff00
3205 2F51 26      80          mvi      h,80h
3206 2F53 C3      2F42      jmp     rmchk2   ; check 8000
3207
3208 2F56 26      FF          mvi      h,0ffh
3209 2F58 01      00FF      lxi      b,0ffh ; check 8155 ram
3210
3211 2F5B 56          rmchk1:  mov      d,m     ; save the original data in D
3212 2F5C AF          xra     a
3213 2F5D 77          mov      m,a     ; 1st write 0
3214 2F5E BE          cmp      m
3215 2F5F C2          jnz     ramerr   ; if not =, then error
3216 2F62 3D          dcr      a
3217 2F63 77          mov      m,a     ; next write FF
3218 2F64 BE          cmp      m
3219 2F65 C2          jnz     ramerr
3220 2F68 3E          mvi      a,5ah
3221 2F6A 77          mov      m,a     ; last write 5A
3222 2F6B BE          cmp      m
3223 2F6C C2          jnz     ramerr   ; if not =, then error
3224 2F6F 72          mov      m,d     ; restore original data
3225 2F70 23          inx     h
3226 2F71 0B          dcx     b      ; dec # of bytes to check
3227 2F72 78          mov      a,b
3228 2F73 B1          ora     c
3229 2F74 C2          jnz     rmchk1
3230 2F77 C3      2F5B      jmp     rmchk1   ; no RAM ERRORS CHECK UART
3231
3232 2F7A CD      011D      ramerr:  call    daddr   ; display the address of error
3233 2F7D 3E      09          mvi     a,9
3234 2F7F CD      00CC      call    regprnt ; show "br" in right two displays
3235 2F82 CD      016C      call    beep
3236 2F85 CD      0133      call    rdkey   ; wait here so displays aren't trashed
3237
3238          ;
3239          ; Now do checks of I/O devices
3240          ;
3241          ; See if there is a UART
3242 2F88 AF          uartch:  xra     a
3243 2F89 32      FFF7      sta     uartflg ; assume there is a uart
3244
3245 2F8C 3E      23          mvi     a,23h   ; enable tx only
3246 2F8E D3      81          out     sercom
3247 2F90 DB      80          in      serdta  ; discard char that might be waiting
3248 2F92 DB      81          in      sercom  ; RxDY must be low now
3249 2F94 E6      02          ani     2       ; see if RxDY is low
3250 2F96 C2      302F      jnz     nouart  ; if high, no UART
3251
3252 2F99 3E      27          mvi     a,27h   ; enable Tx and Rx
3253 2F9B D3      81          out     sercom
3254
3255 2F9D CD      3098      call    randchk ; pause then check for variation
3256 2FA0 C2      302F      jnz     nouart
3257
3258          ; See if transmit works
3259 2FA3 3E      0D          mvi     a,0dh   ; A = CR
3260 2FA5 D3      80          out     serdta
3261 2FA7 D3      80          out     serdta ; fill the double buffer
3262 2FA9 DB      81          in      sercom
3263 2FAB E6      01          ani     1       ; isolate transmit ready bit
3264 2FAD C2      302F      jnz     nouart  ; if txrdy, no uart (it should be full)
3265 2FB0 21      FFFF      lxi     h,0ffffh
3266 2FB3 CD      0181      call    dlay    ; give time to empty
3267 2FB6 2B      81          dcx     h
3268 2FB7 CD      0181      call    dlay
3269
3270 2FBA DB      81          in      sercom
3271 2FBC E6      01          ani     1       ; isolate transmit ready bit
3272 2FBE CA      302F      jz      nouart  ; if not txrdy, no uart
3273
3274 2FC1 3E      26          mvi     a,26h   ; disable transmit
3275 2FC3 D3      81          out     sercom
3276 2FC5 3E      20          mvi     a,' '
3277 2FC7 D3      80          out     serdta  ; send UART a space
3278 2FC9 D3      80          out     serdta
3279 2FCB CD      3098      call    randchk ; pause and check for variation

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3280 2FCE C2      302F      jnz      nouart      ; if fluctuation then no uart
3281
3282 2FD1 3E      27          mvi      a,27h      ; enable transmit again
3283 2FD3 D3      81          out      sercom
3284 2FD5 DB      81          in       sercom      ; if you read this real quick, it shouldn't be empty
3285 2FD7 E6      04          ani      100b      ; isolate txempty bit
3286 2FD9 C2      302F      jnz      nouart      ;if txempty=true then error, because tx is disabled
3287
3288
3289
3290
3291 2FDC 1E      0D          ; check for local loopback
3292 2FDE 21      10BE       mvi      e,cr
3293 2FE1 1D      FFFF       call     conout     ; send CR to console
3294 2FE4 CD      0181       lxi      h,0ffffh
3295 2FE7 DB      81          call     dlay       ; wait for char to finish TXing
3296 2FE9 E6      02          in       sercom      ; get serial port status
3297 2FEB CA      3026       ani      2          ; isolate receive ready bit
3298 2FEE DB      80          jz       looplx     ; if no key, exit
3299 2FF0 FE      0D          in       serdta     ; get key
3300 2FF2 C2      3026       cpi      cr
3301          jnz      looplx     ; if not CR, exit
3302 2FF5 0E      00          ; TX and RX are connected for local loopback. Send 00 to FF
3303 2FF7 21      FFFF       mvi      c,0        ; start with 0
3304 2FFA 79      79          lxi      h,0ffffh  ; timeout delay
3305 2FFB D3      80          out      serdta     ; send c
3306 2FFD DB      81          in       sercom      ; get serial port status
3307 2FFF E6      02          ani      2          ; isolate receive ready bit
3308 3001 C2      300D       jnz      loopl3     ; if key, exit timeout loop
3309 3004 2B      2B          dcx     h           ; dec timeout count
3310 3005 7C      7C          mov     a,h
3311 3006 B5      B5          ora     l           ; see if HL=0
3312 3007 C2      2FFD       jnz      loopl2     ; check stat again
3313 300A C3      3039       jmp     badser      ; if timeout, error
3314
3315 300D DB      80          loopl3: in       serdta ; get key read from UART
3316 300F B9      B9          cmp     c           ; see if same as transmitted
3317 3010 C2      3039       jnz      badser    ; if not same, error
3318 3011 06      06          mvi      b,rgrdsp+4 ; put uart data on left pair
3319 3015 CD      010B      call    disbyt
3320
3321 3018 0C      0C          inr     c
3322 3019 C2      2FF7       jnz      loopl0     ; send next char if not 0
3323
3324 301C 06      85          ; UART tests ok, so disable further tests
3325 301E 3E      0E          mvi      b,rgrdsp+5 ; select left pair
3326 3020 CD      00CE      call    regprnl    ; show L.L. (local loopback)
3327 3023 C3      3044       jmp     disuart     ; disable UART tests
3328
3329 3026 11      2F04       looplx: lxi      d,commsg ;
3330 3026 11      2F04       call    pstrng     ; print "UART test"
3331 3029 CD      10CD       call    iochk      ;
3332 302C C3      3049       jmp
3333
3334 302F 0E      85          nouart: mvi      b,rgrdsp+5 ; select left pair
3335 3031 3E      0E          mvi      a,13      ; show N.U.
3336 3033 CD      00CE      call    regprnl    ;
3337 3036 C3      3044       jmp     disuart
3338
3339 3039 3E      0C          badser: mvi      a,12 ; bad serial port error
3340 303B CD      00CC      call    regprnt    ; display "b.s." on left 2 displays
3341 303E CD      016C      call    beep       ;
3342 3041 CD      0133      call    rdkey      ; wait here so displays aren't trashed
3343
3344 3044 3E      01          disuart: mvi      a,1 ; disable UART test
3345 3046 32      FFF7      sta     uartflg    ; uart flag > 0 if no uart
3346
3347
3348 3049 DB      12          ; This is the main loop
3349 304B D3      11          iochk:  in       dip ; echo dip to leds
3350
3351 304D CD      1191       ; check A/D
3352 3050 7D      7D          call    adcin     ; get A/D input
3353 3051 B7      B7          mov     a,l
3354 3052 3E      40          ora     a,01000000b ; set sod off bit pattern
3355 3054 CA      3059       jz      iochka    ; if l=0 then shut off sod
3356 3057 F6      80          ori     80h       ; set sod on bit pattern
3357 3059 30      30          iochka: sim
3358 305A 65      65          mov     h,l
3359 305B CD      015C      call    sdv       ; send value to 8155 timer for sound
3360 305E 7D      7D          mov     a,l
3361 305F CD      0109      call    ddata     ; display the a/d input on right pair
3362
3363 3062 CD      0129       iochk1: call    plkpad    ; check keypad
3364 3065 FE      FF          cpi     0ffh      ; see if no key pressed
3365 3067 CA      306F      jz      iochk2    ; if no key ready, don't output data
3366 306A 06      82          mvi      b,rgrdsp+2 ; choose middle display
3367 306C CD      010B      call    disbyt
3368
3369
3370 306F 3A      FFF7      iochk2: lda     uartflg ; if there is no uart, skip the following
3371 3072 B7      B7          ora     a
3372 3073 C2      3095       jnz     iochk9
3373
3374 3076 DB      81          in       sercom      ; get serial port status
3375 3078 E6      02          ani      2          ; isolate receive ready bit
3376 307A CA      3095       jz      iochk9    ; if no key ready, continue
3377 307D DB      80          serdta ; get the character
3378 307F 4F      4F          mov     c,a        ; save key in c
3379 3080 06      84          mvi      b,rgrdsp+4 ; put uart data on left pair
3380 3082 CD      010B      call    disbyt
3381
3382 3085 DB      81          in       sercom      ; now send data back to UART
3383 3087 E6      01          ani      1          ; isolate transmit ready bit
3384 3089 CA      3095       jz      iochk9    ; if transmit not ready, don't output
3385 308C 79      79          mov     a,c        ; put char in a
3386 308D D3      80          out     serdta     ; output to terminal
3387 308F 11      2F0D      lxi     d,prmt     ;
3388 3092 CD      10CD      call    pstrng     ; display prompt
3389
3390 3095 C3      3049       iochk9: jmp     iochk   ; do it all again
3391
3392
3393
3394 3098 21      FFFF      ; This checks for changes in the UART status, indicating no UART
3395 309B CD      0181      randchk: lxi     h,0ffffh
3396 309E DB      81          call    dlay
3397 30A0 E6      06          in       sercom      ; read the initial value of sercom
3398 30A2 4F      4F          ani      110b      ; txempty and rxrdy lines
3399 30A3 06      0A          mov     c,a        ; original value is in c
3400 30A5 21      0200      mvi      b,10     ; number of times to check
3401 30A8 CD      0181      randchk1: call    dlay ; pause to allow change
3402 30AB DB      81          in       sercom
3403 30AD E6      06          ani      110b      ; check txempty and rxrdy lines again
3404 30AF B9      B9          cmp     c
3405 30B0 C0      C0          rnz     b          ; if not equal, then return with B > 0
3406 30B1 05      05          dcr     b
3407 30B2 C2      30A5      jnz     rndchk1
3408
3409 30B5 C9      C9          ; return with z=true after 10 loops without a mistake
3410
3411
3412 30B6 01      3FFE      checksm: lxi     b,chksum ; point to end of mem
3413 30B9 0B      0B          etst1:  dcx     b        ; start at byte before
3414 30BA 16      16          mvi      d,0       ; clear upper byte of de
3415 30BC 0A      0A          ldax   b          ; a=byte from (bc)
3416 30BD 5F      5F          mov     e,a
3417 30BE 19      19          dad     d          ; hl=hl+de
3418 30BF 78      78          mov     a,b
3419 30C0 B1      B1          ora     c          ; see if bc=0
3420 30C1 C2      30B9      jnz     etst1
3421 30C4 C9      C9          ret
3422
3423
3424
3425
3426
3427
3428
;=====
; PROGRAM ENTRY MODE
; This allows you to change the contents at the address displayed in the
; left 4 digits. The contents are displayed in the right two digits.
; From this mode you can choose any of the direct commands or functions.

```

```

3429          ;
3430 30C5          EMODE:
3431 30C5 2A      LHLH      PCREG      ; HL=PC
3432 30C8 CD      CALL      DADDR      ; PRINT PC ON LEFT 4 DISPLAYS
3433 30CB 7E      MOV       A,M        ; LOAD DATA FROM PC
3434 30CC CD      CALL      LDATA      ; PRINT DATA AT PC ON RIGHT TWO DISPLAYS
3435 30CF 6E      MOV       L,M        ; L = DATA AT PC
3436
3437 30D0 CD      0133      NXTKEY:  CALL      RDKEY      ; GET A KEY
3438 30D3 FE      17        KEYCHK:  CPI       ENTER     ;
3439 30D5 C2      30E4      JNZ      CHKDEC   ; IF NOT ENTER, CHECK DECREMENT KEY
3440          ; ENTER WAS PRESSED STORE DATA IN L AT PC AND INC PC
3441 30D8 7D      MOV       A,L        ; L IS THE DATA SHOWN IN THE RIGHT 2 DISPLAYS
3442 30D9 2A      LHLH      PCREG      ;
3443 30DC 77      MOV       M,A        ; STORE DATA AT PC
3444 30DD 23      INX      H        ; POINT TO NEXT ADDRESS
3445 30DE 22      SHLD     PCREG      ; SAVE NEW PC
3446 30E1 C3      JMP       EMODE
3447
3448 30E4 FE      16        CHKDEC:  CPI       DECPC    ;
3449 30E6 C2      JNZ      CHKSTP   ; IF NOT DEC, CHECK STEP
3450 30E9 2A      LHLH      PCREG      ;
3451 30EC 2B      DCX      H        ; DEC PCREG
3452 30ED 22      SHLD     PCREG      ;
3453 30F0 C3      JMP       EMODE
3454
3455 30F3 FE      14        CHKSTP:  CPI       STEP     ;
3456 30F5 CA      31B7      JZ       SINGSTP   ; IF NOT STEP, CHECK FOR HEX DIGIT
3457
3458 30F8 FE      10        KEYCHK1: CPI       10H      ;
3459 30FA D2      JNC      CHKCMD   ; IF A<10 THEN CY=1, IT IS A HEX DIGIT
3460          ; A DIGIT WAS TYPED IN THE PROGRAM ENTRY MODE, DISPLAY IT ON THE LEFT 2 DISPLAYS
3461 30FD CD      008A      CALL     DIGIT2   ;
3462 3100 7D      MOV       A,L        ;
3463 3101 CD      0109      CALL     DDATA    ; DISPLAY NEW DATA
3464 3104 C3      JMP       NXTKEY
3465
3466          ; IS IT A COMMAND KEY?
3467 3107 FE      15        CHKCMD:  CPI       FUNC    ;
3468 3109 C2      JNZ      EMODE    ; IF NOT FUNC THERE IS NO OTHER
3469
3470          ;FUNCTION MODE
3471          ; FUNC. PRESSED
3472 310C CD      00C2      CALL     FUNPRNT  ; PRINT FUNC.
3473 310F CD      0133      CALL     RDKEY    ; GET THE NEXT KEY
3474
3475          ; THESE VALUES ARE USED TO SELECT THE REGISTER
3476          ; AF = 0, BC=1, DE=2, HL=3, SP=4,PC=5,BRK = 6, SC = 7
3477 3112 FE      14        CPI       STEP     ; STEP/RUN KEY
3478 3114 CA      3204      JZ       RUN      ;
3479          ; IF NOT RUN CHECK A/F
3480 3117 FE      0A        CPI       0AH      ;
3481 3119 C2      3121      JNZ      EMD1     ; IF NOT A/F CHECK B/C
3482          ; CHANGE AF
3483 311C 3E      MVI      A,0       ; SELECT AF
3484 311E C3      JMP      SHCHNG   ; SHOW VALUE OF REGISTER AND CHANGE IT
3485
3486 3121 FE      0B        EMD1:  CPI       0BH      ;
3487 3123 C2      JNZ      EMD2     ; IF NOT B/C CHECK D/E
3488          ; CHANGE BC
3489 3126 3E      MVI      A,1       ; SELECT BC
3490 3128 C3      JMP      SHCHNG   ; SHOW VALUE OF REGISTER AND CHANGE IT
3491
3492 312B FE      0C        EMD2:  CPI       0CH      ;
3493 312D C2      JNZ      EMD3     ; IF NOT D/E CHECK H/L
3494          ; CHANGE DE
3495 3130 3E      MVI      A,2       ; SELECT DE
3496 3132 C3      JMP      SHCHNG   ; SHOW VALUE OF REGISTER AND CHANGE IT
3497
3498 3135 FE      0D        EMD3:  CPI       0DH      ;
3499 3137 C2      JNZ      EMD4     ; IF NOT H/L CHECK SP
3500          ; CHANGE HL
3501 313A 3E      MVI      A,3       ; SELECT HL
3502 313C C3      JMP      SHCHNG   ; SHOW VALUE OF REGISTER AND CHANGE IT
3503
3504 313F FE      0E        EMD4:  CPI       0EH      ;
3505 3141 C2      JNZ      EMD5     ; IF NOT SP CHECK PC
3506          ; CHANGE SP
3507 3144 3E      MVI      A,4       ; SELECT SP
3508 3146 C3      JMP      SHCHNG   ; SHOW VALUE OF REGISTER AND CHANGE IT
3509
3510 3149 FE      0F        EMD5:  CPI       0FH      ;
3511 314B C2      JNZ      EMD6     ; IF NOT PC CHECK BP
3512          ; CHANGE PC
3513 314E 3E      MVI      A,5       ; SELECT PC
3514 3150 C3      JMP      SHCHNG   ; SHOW VALUE OF REGISTER AND CHANGE IT
3515
3516 3153 FE      08        EMD6:  CPI       8        ;
3517 3155 C2      JNZ      EMD7     ; IF NOT BP CHECK STACK CONTENTS
3518          ; CHANGE BP
3519 3158 3E      MVI      A,6       ;
3520 315A C3      JMP      SHCHNG   ;
3521
3522 315D FE      09        EMD7:  CPI       9        ;
3523 315F C2      JNZ      EMD8     ; IF NOT SC CHECK SELF TEST
3524 3162 3E      MVI      A,7       ; SELECT SC
3525 3164 CD      00CC      CALL     REGPRNT  ; PRINT IT
3526          ; POINT TO BYTE ON TOP OF USER STACK. THIS BYTE AND THE ONE BELOW WILL BE
3527          ; REMOVED WHENEVER THERE IS A POP.
3528 3167 2A      LHLH      SPREG    ;
3529 316A C3      JMP      SHCHNG1  ;
3530
3531 316D FE      01        EMD8:  CPI       1        ;
3532 316F CA      2F11      JZ       SLTST    ; FUNC. 1 = SELF TESTER
3533
3534 3172 FE      03        CPI       3        ;
3535 3174 CA      3300      JZ       CHEXCON  ; FUNC. 3 = RECEIVE HEX FILE FROM SERIAL PORT
3536
3537 3177 FE      04        CPI       4        ;
3538 3179 CA      1814      JZ       EPRMPRO  ; FUNC. 4 = MENU DRIVEN EPROM PROGRAMMER
3539
3540 317C FE      02        CPI       2        ;
3541 317E C2      JNZ      EMODE    ; GO BACK TO ENTRY MODE IF NOT FUNC.2
3542
3543 3181 2A      LHLH      AFREG    ;
3544 3184 E5      PUSH     H        ;
3545 3185 F1      POP      PSW      ; AF = AFREG
3546 3186 2A      LHLH      BCREG    ;
3547 3189 44      MOV      B,H        ; BC = BCREG
3548 318A 4D      MOV      C,L        ;
3549 318B 2A      LHLH      DREG    ;
3550 318E EB      XCHG     ; DE = DREG
3551 318F 2A      LHLH      SPREG    ;
3552 3192 F9      SPHL     ; SP = SPREG
3553 3193 2A      LHLH      PCREG    ;
3554 3196 E5      PUSH     H        ; PUT RET ADDRESS ON STACK FOR MON
3555 3197 2A      LHLH      HLREG    ; HL = HLREG
3556 319A CD      1000      CALL     MSERVICES ; FUNC. 2 = CALL 1000
3557 319D C3      JMP      MON      ; PRETEND WE SINGLE STEPPED
3558
3559          ; SHOW AND CHANGE REG PAIR WHOSE NUMBER IS IN A.
3560          ; AF = 0, BC=1, DE=2, HL=3, SP=4,PC=5,BRK = 6, SC = 7
3561
3562 31A0 57      SHCHNG: MOV      D,A      ; D=REG PAIR
3563 31A1 CD      00CC      CALL     REGPRNT  ; SHOW THE REGISTER NAME
3564 31A4 7A      MOV      A,D      ; RESTORE REG PAIR
3565 31A5 21      LXI     H,AFREG   ;
3566 31A8 0F      RLC      ; A=A*2
3567 31A9 85      ADD     L        ;
3568 31AA 6F      MOV      L,A      ;
3569 31AB 3E      MVI      A,0      ;
3570 31AD 8C      ADC      H        ; HL=HL+2*A
3571 31AE 67      MOV      H,A
3572
3573 31AF E5      SHCHNG1: PUSH     H        ; HL= POINTER LOW BYTE OF REGISTER IN MEMORY
3574
3575 31B0 7E      MOV      A,M
3576 31B1 23      INX     H
3577 31B2 66      MOV      H,M

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3578 31B3 6F          MOV      L,A      ; HL IS NOW DATA AT HL
3579 31B4 C3          JMP      DSPHL
3580
3581
3582
3583 ; SINGSTP: performs a software single step.
3584 ; store ei before the instr then jump to EI
3585 ; it will interrupt after the command is executed
3586 ; ALSO check for calls to rom and put ff after them instead of ei before
3587 31B7 2A          FFF5     singstp:  lhld    pcreg
3588 31BA 7E          FF       mov     a,m      ; get opcode from next byte to execute
3589 31BB FE          FF       cpi     0ffh    ; is the next instruction a RST 7?
3590 31BD CA          30C5    jz      emode    ; if it is, don't execute
3591
3592 31C0 20          0D       rim     1101b   ; clear 6.5 bit
3593 31C1 E6          08       ori     1000b   ; set mask set enable bit
3594 31C3 F6          30       sim     ; enable 6.5 interrupt (the 6.5 pin is tied high)
3595 31C5 30
3596 31C6 21          32A4    lxi    h,mon    ; vector the 6.5 interrupt to MON
3597 31C9 22          FFE7    shld   vec6hlf
3598
3599 31CC 3E          01       mvi    a,1      ; set single step flag
3600 31CE 32          FFFA    sta    sstep    ; store flag
3601
3602 31D1 2A          FFF5     lhld   pcreg    ; get pcreg again
3603 31D4 7E          FF       mov     a,m      ; get opcode from next byte to execute
3604 31D5 FE          CD       cpi     0cdh    ; is it a CALL?
3605 31D7 C2          31F0    jnz    nocall
3606 ; see if it is a call to rom
3607 31DA 23          23       inx    h
3608 31DB 23          23       inx    h        ; point to high byte of address
3609 31DC 7E          FF       mov     a,m      ; A = high byte
3610 31DD FE          40       cpi     maxrom   ; if > maxrom, this is not a rom call
3611 31DF D2          31F0    jnc    nocall
3612 ; this is a rom call
3613 31E2 23          23       inx    h        ; point to address after the call
3614 31E3 7E          FF       mov     a,m
3615 31E4 32          FFFD    sta    sstep    ; save the original value
3616 31E7 22          FFFB    shld   ssadd    ; save the address
3617 31EA 3E          FF       mvi    a,0ffh
3618 31EC 77          FF       mov     m,a     ; store rst 7 at hl
3619 31ED C3          321D    jmp    runl     ; run from here
3620
3621 31F0 2A          FFF5     nocall:  lhld   pcreg    ; point to the byte before next instruction
3622 31F3 2B          2B       dcx    h
3623 31F4 7E          FF       mov     a,m      ; get data
3624 31F5 32          FFFD    sta    sstep    ; save it
3625 31F8 3E          FB       mvi    a,0fbh   ; A= ei instr.
3626 31FA 77          FF       mov     m,a     ; store the ei
3627 31FB 22          FFFB    shld   ssadd    ; save address
3628 31FE 22          FFF5     shld   pcreg    ; pcreg points to the ei
3629 3201 C3          321D    jmp    runl     ; run from here
3630
3631 ;
3632 ; RUN : Replaces the data at the breakpoint with a FF, restores
3633 ; the registers that were stored at MON:
3634 ; and runs from PC
3635 3204 2A          FFF5     run:    lhld   pcreg    ; see if there is a breakpoint
3636 3207 CD          3237    call   chl2bp   ; where we will start executing
3637 320A CA          30C5    jz      emode    ; if BPREG = PCREG then
3638 ; don't execute it
3639 320D 7E          FF       mov     a,m      ; a = next byte to execute
3640 320E FE          FF       cpi     0ffh    ; is it RST 7?
3641 3210 CA          30C5    jz      emode    ; don't execute it
3642
3643 3213 7E          FFF7    lhld   bpreg
3644 3216 2A          FF       mov     a,m      ; a=data from program address (hl)
3645 3217 32          FFF9    sta    bptemp   ; preserve opcode from (hl)
3646 321A 3E          FF       mvi    a,0ffh
3647 321C 77          FF       mov     m,a     ; store a rst 7 at break point
3648 ;restore original values of registers
3649 321D 2A          FFEB    runl:  lhld   afreg
3650 3220 E5          FF       push   h
3651 3221 F1          FF       pop    bsw      ; af is restored
3652 3222 2A          FFEF    lhld   bcreg
3653 3225 44          FF       mov     b,h
3654 3226 4D          FF       mov     c,l     ; bc is restored
3655 3227 2A          FFEF    lhld   dereg
3656 322A EB          FFF3    xchg   ; de is restored
3657 322B 2A          FFF3    lhld   spreg
3658 322E F9          FFF5    sphl   ; sp is restored
3659 322F 2A          FFF5    lhld   pcreg
3660 3232 E5          FFF1    push   h        ; push pc so we can return to this address
3661 3233 2A          FFF1    lhld   hlreg   ; hl is restored
3662 3236 C9          FF       ret     ; return to pc
3663
3664 ; CHL2BP = RETURN Z=1 IF HL = BPREG
3665 3237 3A          FFF7    chl2bp: lda    bpreg
3666 323A BD          FF       cmp    l
3667 323B C0          FF       rnz    ; if L<low byte of bpreg return z=0
3668 ; L = low byte at bpreg
3669 323C 3A          FFF8    lda    bpreg+1
3670 323F BC          FF       cmp    h        ; if H=hi byte of bpreg ret z=1
3671 ; else return z=0
3672 3240 C9          FF       ret
3673
3674 ;
3675 ; This is the entry point for a breakpoint whether it was a single step
3676 ; through a rom call or not.
3677 bentry:
3678 3241 22          FFF1    shld   hlreg   ; save hl
3679 3244 E1          FF       pop    h        ; get ret address
3680 3245 2B          FF       dcx    h        ; point to the rst 7
3681 3246 22          FFF5    shld   pcreg   ; save pc
3682
3683 ; clear breakpoint address and replace original data
3684 3249 F5          3237    push   psw     ; this will be POpEd into hl at monl:
3685 324A CD          3237    call   chl2bp
3686 ; if there was a hand placed FF or a single step over a call to ROM and
3687 ; it wasn't at the breakpoint address then don't clear the breakpoint
3688 324D C2          32AC    jnz    monl
3689
3690 3250 3A          FFF9    lda    bptemp  ; a= data that was replaced by ff
3691 3253 2A          FFF7    lhld   bpreg   ; point to the break point
3692 3256 77          FF       mov     m,a     ; restore it to the break point
3693 3257 21          0000    lxi    h,0
3694 325A 22          FFF7    shld   bpreg   ; clear breakpoint address
3695 325D C3          32AC    jmp    monl
3696
3697 ; initialize monitor
3698 ; in case reset was pressed after a breakpoint was selected
3699 ; or during single step, restore value at breakpoint
3700
3701 init_mon:
3702 3260 2A          FFF7    lhld   bpreg
3703 3263 3A          FFF9    lda    bptemp
3704 3266 77          FF       mov     m,a
3705 3267 2A          FFFB    lhld   ssadd
3706 326A 3A          FFFD    lda    sstep
3707 326D 77          FF       mov     m,a
3708
3709 326E AF          FFFA    xra    a        ; clear single step flag
3710 326F 32          0000    sta    sstep
3711 3272 21          FFF7    lxi    h,0
3712 3275 22          FFF7    shld   bpreg   ; set bp in rom so it is ineffective
3713 3278 22          FFFB    shld   ssadd   ; set single step in rom also
3714 ; clear user registers
3715 327B 22          FFEF    shld   afreg
3716 327E 22          FFEF    shld   bcreg
3717 3281 22          FFEF    shld   dereg
3718 3284 22          FFF1    shld   hlreg
3719
3720 3287 31          FFD6    lxi    sp,userstk
3721 328A E5          FF       push   h        ; push a zero
3722 328B 39          FF       dad    sp       ; hl= sp
3723 328C 22          FFF3    shld   spreg   ; store in spreg
3724
3725 328F 21          0000    lxi    h,0

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3727 3292 31      FFEB          lxi      sp,monstk+6
3728 3295 E5          push     h          ; set 5.5 vector to 0
3729 3296 E5          push     h          ; set 6.5 vector to 0
3730 3297 E5          push     h          ; set 7.5 vector to 0
3731
3732          ;
3733          ; start user program at begram+1 so single step will work on the
; first instruction (this makes room for an EI).
3734 3298 21      FF01          lxi      h,begram+1
3735 329B 22      FFF5          shld    pcreg
3736
3737 329E CD      016C          call    beep
3738 32A1 C3      30C5          jmp     emode      ; go to entry mode
3739
3740          ; SAVE REGS IN RAM
mon:
3741 32A4 22      FFF1          mon:    shld    HLREG      ; hl is saved
3742 32A7 E1          POP     H          ; HL=RET ADDRESS
3743 32A8 22      FFF5          shld    pcreg      ; pc is saved
3744 32AB F5          push    psw
3745
3746 32AC E1          mon1:   pop     h          ; hl = af
3747 32AD 22      FFE6          shld    afreg      ; af is saved
3748 32B0 EB          FFE6          XCHG    ; HL=DE
3749 32B1 22      FFEF          SHLD    DEREg     ; de is saved
3750 32B4 60          MOV     H,B
3751 32B5 69          MOV     L,C      ; HL=BC
3752 32B6 22      FFE6          SHLD    BCREG     ; bc is saved
3753 32B9 21      0000          LXI     H,0
3754 32BC 39          DAD    SP        ; HL=SP
3755 32BD 22      FFF3          SHLD    SPREG     ; sp is saved
3756
3757 32C0 31      FFE5          lxi     sp,monstk      ; point to monitor stack
3758
3759          ; check for single step flag
3760 32C3 3A      FFFA          lda     sstep
3761 32C6 B7          ora     a
3762 32C7 CA      30C5          jz     emode      ; if not single step go to entry mode
3763 32CA E6          rim
3764 32CB 20          OF          ani     1111b     ; we only want interrupt mask status
3765 32CD F6          0A          ori     1010b    ; a 1 disables the interrupt
3766 32CF 30          sim
3767 32D0 AF          xra     a
3768 32D1 32      FFFA          sta     sstep     ; clear single step flag
3769 32D4 2A      FFFB          lhld   ssadd     ; hl= address of ei or rst 7
3770 32D7 3A      FFFD          lda     sstemp    ; get original data
3771 32DA 77          mov     m,a
3772 32DB C3      30C5          jmp     emode
3773
3774 32DE CD      011D          dsphl:  call    daddr     ; display hl
3775 32E1 CD      0133          call    rdkey
3776 32E4 FE      17          cpi     enter
3777 32E6 CA      32F8          jz     entreg     ; if enter, change reg to value of hl
3778 32E9 FE      10          cpi     10h
3779 32EB D2      32F4          jnc    otherk    ; if >= 10 then it isn't a digit
3780 32EE CD      0093          call    digit4
3781 32F1 C3      32DE          jmp     dsphl     ; display new hl
3782
3783 32F4 E1          otherk: pop     h          ; clean stack
3784 32F5 C3      30D3          jmp     keychk    ; see what command it was and do it
3785
3786 32F8 EB          entreg: xchg    ; de=hl
3787 32F9 E1          pop     h          ; hl points to low byte of register pair
3788 32FA 73          mov     m,e
3789 32FB 23          inx    h
3790 32FC 72          mov     m,d      ; de is saved in the selected ram register
3791 32FD C3      30C5          jmp     emode
3792
3793          ;
3794          ; RECEIVE AN INTEL HEX FILE
3795          ;
3796 3300          CHEXCON:
3797 3300 11      0504          LXI     D,0504H   ; START AT DISPLAY 5 AND OUTPUT 4 CHARS
3798 3303 21      3323          LXI     H,CHEXMS1
3799 3306 CD      148A          CALL    LEDSTR    ; SHOW 'REC.'
3800 3309 CD      332A          CALL    HEXCON
3801 330C E6          ANI     11110B   ; MASK OFF ALL BUT ERROR BITS
3802 330E CA      3320          JZ     CHEXC�1
3803 3311 CD      0109          CALL    DDATA     ; SHOW ACCUM IN RIGHT PAIR OF DISPLAYS
3804 3314 11      0503          LXI     D,0503H   ; START AT DISPLAY 5 AND OUTPUT 3 CHARS
3805 3317 21      3327          LXI     H,CHEXMS2
3806 331A CD      148A          CALL    LEDSTR
3807 331D CD      0133          CALL    RDKEY     ; PAUSE FOR KEY
3808 3320 C3      30C5          CHEXC�1: JMP     EMODE
3809
3810 3323 05      97 9B 08     CHEXMS1: DB     5,97H,9BH,8 ; 'rEC..'
3811 3327 97      05 05        CHEXMS2: DB     97H,5,5 ; 'Err.'
3812          ;
;THIS ROUTINE LOADS A INTEL HEX FILE INTO THE PRIMER'S MEMORY.
;THE TOP OF THE STACK SHOULD HAVE THE ADDRESS WHERE THE FIRST LINE OF THE HEX
;FILE WILL BE STORED. THIS IS POPED INTO DE THEN DE IS CHANGED TO A DIS-
;PLACEMENT VALUE BY SUBTRACTING THE FIRST HEX FILE LOADING ADDRESS FROM DE.
;THE DISPLACEMENT OF DE IS ADDED TO THE LOAD ADDRESSES OF EACH LINE OF HEX.
;IF THE LAST HEX LINE'S DATA LENGTH IS 0 THEN HL WILL CONTAIN THE OPTIONAL
;STARTING ADDRESS AS GIVEN BY THE LAST HEX LINE.
3820          ;
;AFTER THE ROUTINE RETURNS, THE BITS IN E INDICATE THE FOLLOWING ERRORS, IF SET
;BIT# = ERROR
3822          ;1 = ERROR
3823          ;1 = CHECKSUM ERROR
3824          ;2 = NON HEX CHAR ENCOUNTERED
3825          ;3 = ESC CHARACTER ENCOUNTERED.
3826
3827          ;THE BITS IN E INDICATE THE FOLLOWING CONDITIONS
;BIT# = CONDITION
3828          ;0 = THIS IS RECORD TYPE 1 (AN END RECORD)
3829          ;6 = IF SET, DE CONTAINS THE STARTING ADDRESS
3831          ; = IF RESET, DE CONTAINS THE DISPLACEMENT
3832          ;7 = THIS IS THE LAST HEX LINE. HL CONTAINS THE START ADDRESS
3833
3834          ;AFTER AN ERROR OCCURS, HL CONTAINS THE VALUE IT HAD AFTER THE ERROR OCCURED
3835
3836 332A          HEXCON:
3837 332A 11      0000          LXI     D,0
3838
3839 332D          HEX1CON:
3840 332D F3          DI     ;DISABLE INTERRUPTS
3841
3842 332E 3E      00          STRT1:  MVI     A,0
3843 3330 F5          PUSH    PSW      ;SAVE ERROR FLAGS ON STACK ;SET BIT 6,SO DE CONTAINS THE DISP
3844
3845
3846 3331          GETCOLON:
3847 3331 CD      33F6          CALL    GETCHAR
3848 3334 FE      1B          CPI     1BH      ;===TEST
3849 3336 CA      33B1          JZ     ESC1     ;IF ESC,QUIT
3850 3339 FE      3A          CPI     ':'      ;IS IT A COLON?
3851 333B C2      3331          JNZ    GETCOLON ;IF NOT, GET THE NEXT CHAR
3852
3853 333E 0E      00          MVI     C,0      ;CLEAR RUNNING SUM
3854
3855          ;GET THE RECORD LENGTH
3856 3340 CD      33B8          CALL    HEX2BIN
3857 3343 CA      33B1          JZ     ESC1     ;IF ESC, QUIT
3858
3859 3346 47          MOV     B,A      ;B = RECORD LENGTH
3860
3861 3347 81          ADD     C        ;ADD TO RUNNING SUM
3862 3348 4F          MOV     C,A      ;STORE IN C
3863
3864          ;GET THE LOAD ADDRESS AND STORE IN HL
3865 3349 CD      33B8          CALL    HEX2BIN
3866 334C CA      33B1          JZ     ESC1     ;IF ESC, QUIT
3867 334F 67          MOV     H,A      ;SAVE THE START ADDRESS HI
3868
3869          ;ADD TO RUNNING SUM
3870 3350 81          ADD     C        ;ADD TO RUNNING SUM
3871 3351 4F          MOV     C,A      ;STORE IN C
3872
3873 3352 CD      33B8          CALL    HEX2BIN
3874 3355 CA      33B1          JZ     ESC1     ;IF ESC, QUIT
3875 3358 6F          MOV     L,A      ;SAVE THE START ADDRESS LO

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3876
3877 3359 81      ADD      C      ;ADD TO RUNNING SUM
3878 335A 4F      MOV      C,A    ;STORE IN C
3879
3880
;SEE IF DE HAS BEEN CHANGED FROM STORAGE ADDRESS TO DISPLACEMENT
3881 335B F1      POP      PSW    ;GET DE DISPLACEMENT FLAG
3882 335C F5      PUSH     PSW
3883 335D E6      ANI     40H    ;SEE IF BIT 6 IS SET
3884 335F CA      JZ      336C   ;IF NOT SET, ADD DISPLACEMENT
3885
3886 3362 F1      POP      PSW    ;GET ERRORS FROM STACK
3887 3363 EE      XRI     40H    ;RESET DISP FLAG
3888 3365 F5      PUSH     PSW    ;SAVE DISP FLAG
3889
;DE=DE-HL TO GET THE OFFSET THAT WILL BE ADDED TO THE
;POINTER TO STORE THE DATA.
3890
3891
3892 3366 7B      MOV      A,E
3893 3367 95      SUB      L
3894 3368 5F      MOV      E,A
3895 3369 7A      MOV      A,D
3896 336A 9C      SBB     H
3897 336B 57      MOV      D,A  ; DE=DE-HL ?Y
3898
3899 336C          DISPLACE:
3900 336C 19      DAD      D      ;ADD OFFSET TO START ADDRESS
3901
3902
;GET THE RECORD TYPE
3903 336D CD      33B8    CALL     HEX2BIN
3904 3370 CA      33B1    JZ      ESC1   ;IF ESC, QUIT
3905 3373 FE      01      CPI     1      ;IF A=1, IT IS AN END RECORD
3906 3375 C2      337E    JNZ     SKP1
3907
3908 3378 F1      POP      PSW    ;GET ERRORS FROM STACK
3909 3379 F6      ORI     1      ;SET BIT 0, INDICATE END RECORD
3910 337B F5      PUSH     PSW
3911
3912 337C 3E      01      MVI     A,1    ;RESTORE A FOR CHECKSUM ADDING
3913
3914 337E          SKP1:
3915 337E 81      ADD      C      ;ADD TO RUNNING SUM
3916 337F 4F      MOV      C,A    ;STORE IN C
3917
3918 3380 AF      XRA     A
3919 3381 B8      CMP     B      ;IF B=0 THERE IS NO DATA
3920 3382 C2      JNZ     NEXTRD ;IF B<>0 THEN READ THE DATA
3921
3922 3385 F1      POP      PSW    ;GET ERRORS FROM STACK
3923 3386 F6      ORI     80H    ;SET BIT 7 OF E
3924 3388 F5      PUSH     PSW
3925
3926 3389 C3      339A    JMP     CHECKSUM ;GET THE CHECKSUM
3927
;READ AND CONVERT THE HEX FILE DATA
3928
3929
3930 338C          NEXTRD:
3931 338C CD      33B8    CALL     HEX2BIN
3932 338F CA      33B1    JZ      ESC1   ;IF ESC, QUIT
3933 3392 77      MOV      M,A    ;STORE AT HL
3934 3393 81      ADD      C
3935 3394 4F      MOV      C,A    ;KEEP SUM IN C
3936
3937 3395 23      INX     H      ;POINT TO THE NEXT STORAGE
3938 3396 05      DCR     B      ;DECREMENT RECORD COUNTER
3939 3397 C2      338C    JNZ     NEXTRD ;READ THE NEXT BYTE
3940
3941
;GET THE CHECKSUM BYTE AND ADD IT TO THE RUNNING SUM
3942
3943 339A          CHECKSUM:
3944 339A CD      33B8    CALL     HEX2BIN
3945 339D CA      33B1    JZ      ESC1   ;IF ESC, QUIT
3946 33A0 81      ADD      C
3947 33A1 CA      33A8    JZ      CHEKERR ;IF RESULT IS 0, DATA IS CORRECT
3948
3949 33A4 F1      POP      PSW    ;GET ERRORS FROM STACK
3950 33A5 F6      ORI     2      ;SET BIT 1= CHECKSUM ERROR
3951 33A7 F5      PUSH     PSW
3952
3953 33A8          CHEKERR:
3954 33A8 F1      POP      PSW    ;GET ERRORS FROM STACK
3955 33A9 B7      ORA     A      ;SEE IF A=0 (NO ERRORS)
3956 33AA F5      PUSH     PSW
3957 33AB CA      3331    JZ      GETCOLON ;IF NO ERRORS, GET NEXT HEX LINE
3958 33AE C3      33B5    JMP     NOESCEXIT ;NO ESC ENCOUNTERED
3959
3960 33B1          ESC1:
3961 33B1 F1      POP      PSW    ;GET ERRORS FROM STACK
3962 33B2 F6      ORI     8      ;SET BIT 3= ESC KEY FOUND
3963 33B4 F5      PUSH     PSW
3964
3965 33B5          NOESCEXIT:
3966 33B5 FB      EI      ;ENABLE INTERRUPTS
3967 33B6 F1      POP      PSW    ;GET ERRORS FROM STACK
3968 33B7 C9      RET
3969
; ASCII TO HEX CONVERSION
; THIS SUBROUTINE CHANGES ASCII CHARS
; FROM GETCHAR TO BINARY STORED IN THE ACCUMULATOR
; ZF=1 IF ESC KEY ENCOUNTERED AND 0 OTHERWISE
3970
3971
3972
3973
3974
3975
3976 33B8          HEX2BIN:
3977 33B8 C5      PUSH     B
3978 33B9 0E      MVI     C,0    ;CLEAR C
3979 33BB 06      MVI     B,2    ;# OF CHARS TO CONVERT
3980
3981 33BD          NEXTCHR:
3982 33BD CD      33F6    CALL     GETCHAR ;PUT CHAR IN A
3983 33C0 CA      33F4    JZ      ESCEXIT ;EXIT IF ESC CHAR ENCOUNTERED
3984
3985 33C3 FE      30      CPI     '0'    ;SEE IF GREATER THAN 0
3986 33C5 DA      33EB    JC      HEXERR ;IF LESS THAN 0 THEN GIVE ERROR CODE
3987 33C8 FE      3A      CPI     ':'    ; ':' IS THE CHAR AFTER '9'
3988 33CA D2      33D2    JNC     LETTERS ;IF NOT 0..9, SEE IF A..F
3989
3990 33CD E6      0F      ANI     0FH    ;MASK OFF THE 1ST NIBBLE
3991 33CF C3      33DE    JMP     SKIP   ;SKIP OVER LETTER CONVERSION
3992
3993 33D2 FE      41      CPI     'A'    ;SEE IF GREATER THAN A;
3994 33D4 DA      33EB    JC      HEXERR ;IF < A AND > 0 THEN ERROR
3995 33D7 FE      47      CPI     'G'    ;SEE IF A..F
3996 33D9 D2      33EB    JNC     HEXERR ;IF NOT < G GIVE AN ERROR
3997
3998 33DC D6      37      SUI     37H   ;CONVERT TO BINARY ('A'=101 - 91 = 10)
3999
4000 33DE B1          SKIP:
4001 33DF 05      ORA     C      ;C=HI NIBBLE MASKED INTO A
4002 33E0 CA      33F3    DCR     B      ;ITERATION COUNTER
4003
4004 33E3 07      RLC     ;SHIFT TO THE HI NIBBLE
4005 33E4 07      RLC
4006 33E5 07      RLC
4007 33E6 07      RLC
4008
4009 33E7 4F      MOV      C,A    ;PRESERVE 1ST NIBBLE
4010
4011 33E8 C3      33BD    JMP     NEXTCHR ;DO THE NEXT CONVERSION
4012
4013 33EB          HEXERR:
4014 33EB B      POP      B      ;REMOVE BC FROM STACK
4015 33EC D      POP      D      ;REMOVE RETURN ADDRESS FROM STACK
4016 33ED F1      POP      PSW    ;GET ERRORS FROM STACK
4017 33EE F6      ORI     4      ;SET BIT 2=NON HEX ERROR
4018 33F0 F5      PUSH     PSW    ;RESTORE ERRORS TO STACK
4019 33F1 D5      PUSH     D      ;PUT RETURN ADDRESS ON STACK
4020 33F2 C5      PUSH     B      ;PUT BC BACK
4021
4022
4023 33F3 04      FINISH: INR     B      ;CLEAR ZF TO INDICATE NO ESC FOUND
4024 33F4 C1      ESCEXIT: POP     B      ;RESTORE BC PAIR

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4025 33F5 C9                RET
4026
4027                ;
4028                ; Get a char from the serial port and return it in Accumulator.
4029                ;
4030 33F6 DB                81    GETCHAR: in    sercom    ; get serial port status
4031 33F8 E6                02    ; ani    2        ; isolate receive ready bit
4032 33FA CA                33F6   ; jz    getchar  ; loop until bit set
4033 33FD DB                80    ; in    serdta   ; get the character
4034 33FF C9                ; ret
4035
4036
4037                org    chksum
4038 3FFE 7357            dw    07357h    ; 2's comp of sum of data before this
4039
4040                org    monstk
4041                ;
4042                ; SYSTEM VARIABLES
4043
4044 FFE5                vec5hlf: defs 2        ; vector for 5.5 interrupt
4045 FFE7                vec6hlf: defs 2        ; vector for 6.5 interrupt
4046 FFE9                vec7hlf: defs 2        ; vector for 7.5 interrupt
4047 FFE8                afreg:  defs 2        ; af
4048 FFE9                bcreg:  defs 2        ; bc
4049 FFEF                dereg:  defs 2        ; de
4050 FFF1                hlreg:  defs 2        ; hl
4051 FFF3                spreg:  defs 2        ; sp
4052 FFF5                CLKDUM:  ; dummy register to store data in RTC
4053 FFF5                pcreg:  defs 2        ; pc
4054 FFF7                uartflg: ; used during self test to tell that UART exists
4055 FFF7                bpreg:  defs 2        ; break point
4056 FFF9                bptemp: defs 1        ; byte that was replaced by FF
4057 FFFA                SSTEP:  defs 1        ; SINGLE STEP FLAG
4058 FFFB                ssadd:  defs 2        ; address of the ei or ff instruction
4059 FFFD                sstemp:  defs 1        ; holds value replaced by ei or rst 7 instructions
4060 FFFE                ETYP:   defs 1        ; EPROM type for EPROM programmer
4061

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